Advanced Validation Labs, Inc.
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Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S7200AP (Adams Pass)** Test Result: **Pass**

Leveraged System(s): HNS7200AP,HNS7200APL,S7200AP,S7200APF,S7200APL

Modules Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM26LQ4/64HCM	LRDIMM	1.2V	64GB	8Gx72	2666	19	D2	QR
DRAM Vendor	**DRAM Part Number**	**DRAM Density / Width / Date Code**			**Register Vendor / Rev.**		**DIMM Composition**		
SK Hynix	H5ANAG4NCPR-VKC	8Gb	2048Mx4bit	1913	Montage	A0	(2048Mx4)*4*72		

System Configuration

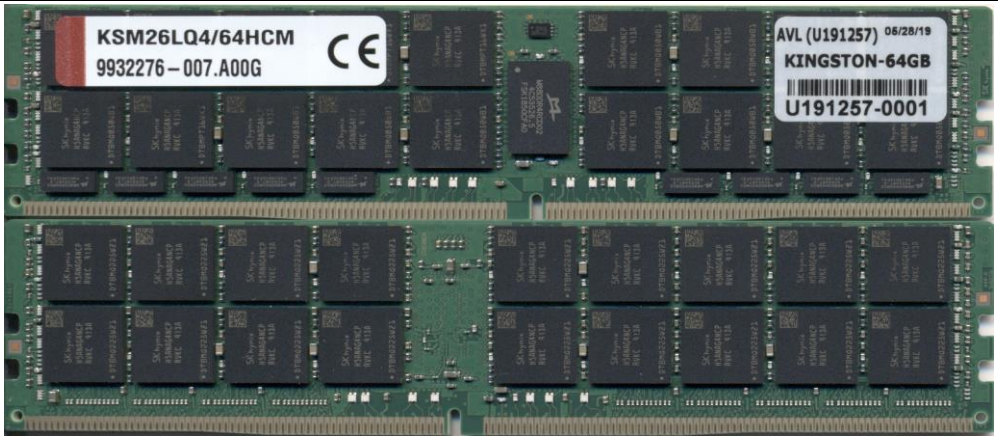
SETUP	System #1	System #2
AVL S/N	S22021	S22020
System S/N	QSAD63905310 / Low	QSAD63905435 / High
Board Rev. (PBA)	G94286-304	
CPU Type	Xeon PHI 7250 / 1.4 GHz	
Chipset	C612	
BIOS	01.02.0041	
BMC / ME	01.01.10452 / 03.01.03.018	
FUR/SDR	1.25	
OS	Linux CentOS 7.3	
Test Tool	iLvss, MCELog	

Testing Summary

Test Items	Test Description		Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS		Done
2. SPD Check	DIMM SPD content check for JEDEC compliance		Pass
3. Memory Stress	Test for 6 hours @ Max Loading	HVDD Hot	Pass
4. Memory Stress		HVDD Cold	Pass
5. Memory Stress		LVDD Hot	Pass
6. Memory Stress		LVDD Cold	Pass

Note:

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	07/12/2019	AVL A#	U191257	AVL W/O	WF2504

Comments:

Test Results

4C Maximum Loading					
Start Date		7/8/2019			
DIMM Voltage		1.22v			
DIMM VPP		2.64v			
DIMM	S/N	A	B	C	D
CPU1 A1	56-004	P	P	P	P
CPU1 B1	56-005	P	P	P	P
CPU1 C1	56-006	P	P	P	P
CPU1 D1	57-004	P	P	P	P
CPU2 E1	57-005	P	P	P	P
CPU2 F1	57-006	P	P	P	P

4C Maximum Loading					
Start Date		7/8/2019			
DIMM Voltage		1.16v			
DIMM VPP		2.422v			
DIMM	S/N	E	F	G	H
CPU1 A1	56-001	P	P	P	P
CPU1 B1	56-002	P	P	P	P
CPU1 C1	56-003	P	P	P	P
CPU1 D1	57-001	P	P	P	P
CPU2 E1	57-002	P	P	P	P
CPU2 F1	57-003	P	P	P	P