



**Advanced Validation Labs, Inc.**  
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**Intel PCSD Server Memory Compatibility Test Certificate**

Test System: <b>Intel S2600BP (Buchanan Pass)</b>	Test Result: <b>Pass</b>
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Leveraged System(s): BBS2600BPB,BBS2600BPQ,BBS2600BPS,HNS2600BPB,HNS2600BPB24,HNS2600BPQ,HNS2600BPQ24,HNS2600BPS,HNS2600BPS24,S2600BP

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM32RD8/32HCR	RDIMM	1.2V	32GB	4Gx72	3200	22	E4	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
SK Hynix	H5ANAG8NCJR-XNC	16Gb	2048Mx8bit	2147	Rambus	B0	(2048Mx8)*2*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	H170047-003	H170047-004
System S/N	QSBP83210875	QSBP71001654
Board Rev. (PBA)	H87926-553	H87926-501
CPU Type	XeonGold 5215M/2.50 GHz Xeon Gold 6252 / 2.1 GHz	
Chipset	Intel 62X Series	
BIOS	02.01.5010	02.01.0012
BMC / ME	2.48 / 04.01.04.381	
FUR/SDR	1.45	
OS	Windows Server 2016 Standard	
Test Tool	iWVSS 2.9.2, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot <span style="color: green;">Pass</span>
4. Memory Stress		HVDD Cold <span style="color: green;">Pass</span>
5. Memory Stress		LVDD Hot <span style="color: green;">Pass</span>
6. Memory Stress		LVDD Cold <span style="color: green;">Pass</span>
Note:		

Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	4/8/2022	AVL A#	U221239	AVL W/O	WG1295
Comments:							

Test Results

4C Minimum Loading					4C Maximum Loading						
Start Date		3/29/2022			Start Date		03/29/22				
DIMM Voltage		1.22v / 1.16v			DIMM Voltage		1.22v / 1.16v				
DIMM	S/N	A	B	C	D	DIMM	S/N	A	B	C	D
CPU1 A1	0001	P	P	P	P	CPU1 A1	0001	P	P	P	P
CPU1 A2						CPU1 A2	0002	P	P	P	P
CPU1 B1	0002	P	P	P	P	CPU1 B1	0003	P	P	P	P
CPU1 C1	0003	P	P	P	P	CPU1 C1	0004	P	P	P	P
CPU1 D1	0004	P	P	P	P	CPU1 D1	0005	P	P	P	P
CPU1 D2						CPU1 D2	0006	P	P	P	P
CPU1 E1	0005	P	P	P	P	CPU1 E1	0007	P	P	P	P
CPU1 F1	0006	P	P	P	P	CPU1 F1	0008	P	P	P	P
CPU2 G1	0007	P	P	P	P	CPU2 G1	0009	P	P	P	P
CPU2 G2						CPU2 G2	0010	P	P	P	P
CPU2 H1	0008	P	P	P	P	CPU2 H1	0011	P	P	P	P
CPU2 I1	0009	P	P	P	P	CPU2 I1	0012	P	P	P	P
CPU2 J1	0010	P	P	P	P	CPU2 J1	0013	P	P	P	P
CPU2 J2						CPU2 J2	0014	P	P	P	P
CPU2 K1	0011	P	P	P	P	CPU2 K1	0015	P	P	P	P
CPU2 L1	0012	P	P	P	P	CPU2 L1	0016	P	P	P	P