



Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600CW (Cottonwood Pass)	Test Result: Pass

Leveraged System(s): S2600C2S,S2600CW,S2600CW2,S2600CW2R,S2600CW2SR,S2600CWT,S2600CWTR,S2600CWTS,S2600CWTSR

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM24RS4/16HDI	RDIMM	1.2V	16GB	2Gx72	2400	17	C	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Micron	H5AN8G4NDJR-XNC	8Gb	2048Mx4bit	2008	IDT	B1	(2048Mx4)*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	SU9557	SU9556
System S/N	FWCT44150269 / LVPP	FWCT44150034 / HVPP
Board Rev. (PBA)	H44615-350	
CPU Type	E5-2680 v4 / 2.40 GHz	
Chipset	C610	
BIOS	01.01.0028	
BMC / ME	1.56 / 051	
FUR/SDR	1.14	
OS	Windows Server 2012 R2	
Test Tool	iVSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot Pass
4. Memory Stress		HVDD/HVPP Cold Pass
5. Memory Stress		LVDD/LVPP Hot Pass
6. Memory Stress		LVDD/LVPP Cold Pass
Note:		

Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	03/05/2021	AVL A#	U201686	AVL W/O	WF8328
Comments:							

Test Results

4C Minimum Loading					
Start Date		3/1/2021			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	0009	P	P	P	P
CPU1 A2					
CPU1 B1	0010	P	P	P	P
CPU1 B2					
CPU1 C1	0011	P	P	P	P
CPU1 C2					
CPU1 D1	0012	P	P	P	P
CPU1 D2					
CPU2 E1	0013	P	P	P	P
CPU2 E2					
CPU2 F1	0014	P	P	P	P
CPU2 F2					
CPU2 G1	0015	P	P	P	P
CPU2 G2					
CPU2 H1	0016	P	P	P	P
CPU2 H2					

4C Maximum Loading					
Start Date		03/01/21			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	0009	P	P	P	P
CPU1 A2	0010	P	P	P	P
CPU1 B1	0011	P	P	P	P
CPU1 B2	0012	P	P	P	P
CPU1 C1	0013	P	P	P	P
CPU1 C2	0014	P	P	P	P
CPU1 D1	0015	P	P	P	P
CPU1 D2	0016	P	P	P	P
CPU2 E1	0017	P	P	P	P
CPU2 E2	0018	P	P	P	P
CPU2 F1	0019	P	P	P	P
CPU2 F2	0020	P	P	P	P
CPU2 G1	0021	P	P	P	P
CPU2 G2	0022	P	P	P	P
CPU2 H1	0023	P	P	P	P
CPU2 H2	0024	P	P	P	P