



Advanced Validation Labs, Inc.

17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600KP (Kennedy Pass)	Test Result: Pass

Leveraged System(s): H2216xxKR2,H2312xxKR2,HNS2600KP,HNS2600KPF,HNS2600KPF,R,HNS2600KPR,S2600KP,S2600KPF,S2600KPF,R,S2600KPR,S2600KPT,S2600KPTR

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM32RD8/16HDR	RDIMM	1.2V	16GB	2Gx72	3200	22	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
SK Hynix	H5AN8G8NDJR-XNC	8Gb	1024Mx8bit	2013	Rambus	B	(1024Mx8)*2*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	SU9568	SU9569
System S/N	BQKP42400310 / LVPP	BQKP42400473 / HVPP
Board Rev. (PBA)	H13888-301	
CPU Type	E5-2660 v4 / 2.0 GHz	
Chipset	C610	
BIOS	01.01.0028	
BMC / ME	1.56 / 051	
FUR/SDR	1.16	
OS	Windows Server 2012 R2	
Test Tool	iVSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot Pass
4. Memory Stress		HVDD/HVPP Cold Pass
5. Memory Stress		LVDD/LVPP Hot Pass
6. Memory Stress		LVDD/LVPP Cold Pass
Note:		

Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	3/15/2021	AVL A#	U201535	AVL W/O	WF8205
Comments:							

Test Results

4C Maximum Loading						
Start Date		3/8/2021				
DIMM Voltage		1.22v				
DIMM VPP		2.64v				
DIMM	S/N	A	B	C	D	
CPU1 A1	0018	P	P	P	P	
CPU1 B1	0019	P	P	P	P	
CPU1 C1	0020	P	P	P	P	
CPU1 D1	0021	P	P	P	P	
CPU2 E1	0022	P	P	P	P	
CPU2 F1	0023	P	P	P	P	
CPU2 G1	0024	P	P	P	P	
CPU2 H1	0025	P	P	P	P	

4C Maximum Loading						
Start Date		3/8/2021				
DIMM Voltage		1.16v				
DIMM VPP		2.422v				
DIMM	S/N	E	F	G	H	
CPU1 A1	0018	P	P	P	P	
CPU1 B1	0019	P	P	P	P	
CPU1 C1	0020	P	P	P	P	
CPU1 D1	0021	P	P	P	P	
CPU2 E1	0022	P	P	P	P	
CPU2 F1	0023	P	P	P	P	
CPU2 G1	0024	P	P	P	P	
CPU2 H1	0025	P	P	P	P	