

Server Memory Compatibility Test Certificate

Test System: **Supermicro X10DRi** Test Result: **Pass**

Leveraged System(s): X10DRi-T, X10DAi, X10DAC, X10DRH-CT, X10DRW-i / X10DRW-IT, X10DDW-I / X10DDW-IN, X10DRG-H / X10DRG-HT, X10DRT-P / X10DRT-PT, X10DRT-PIBQ / X10DRT-PIBF, X10DRFR / X10DRFR-T / X10DRH-C, X10DRH-CLN4, X10DRH-iLN4, X10DRT-PS, X10DRX, X10DRU-X, X10DRU-XLL, X10DRFF-CG, X10DRFF-CTG, X10DRFF-IG,

Module Specifications

Rev. 1/13/2015

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM24RD8/16MEI	RDIMM	1.20V	16GB	2Gx72	2400	19	E2	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code		Register Vendor / Rev.		DIMM Composition			
Micron	MT40A1G8SA-075:E	8Gb	1024Mx8bit	1804	IDT	C0	(2048Mx8)*72		

System Configuration

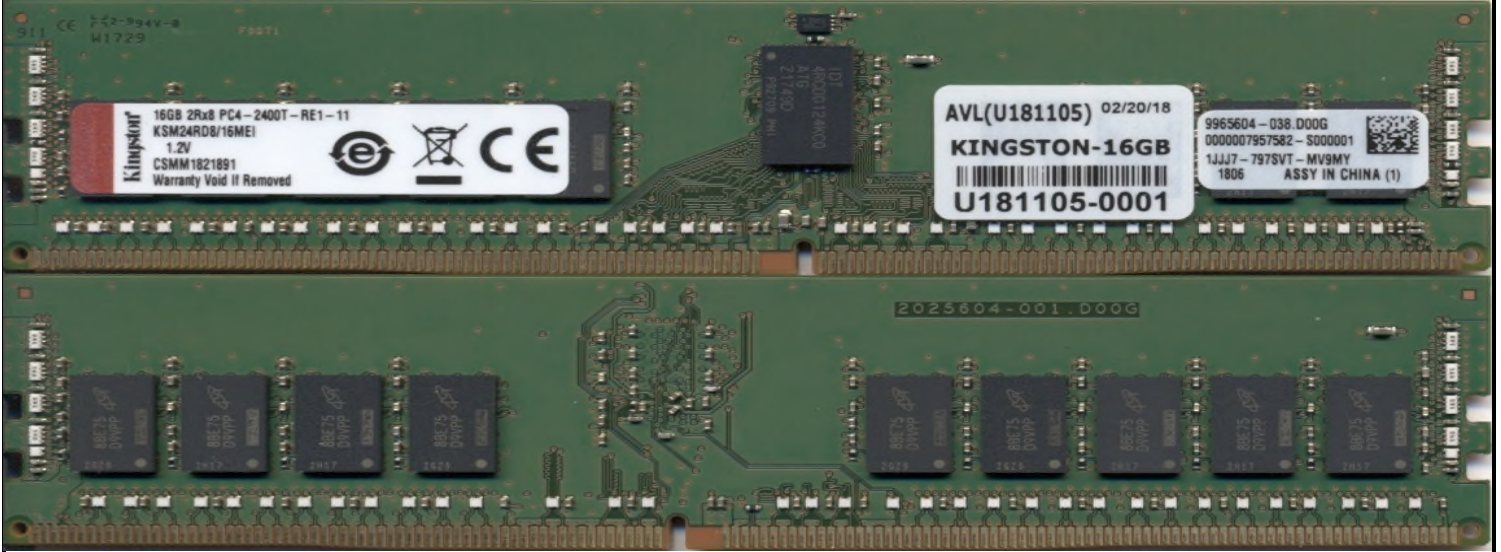
SETUP	System Information
AVL S/N	SV2271
System S/N	0M148004057
Board Rev. (PBA)	1.01
CPU Type	Intel E5-2650 v4 (BDW) x2
Chipset	Intel C612
BIOS / Date	2.1a / 5/5/17
BMC / ME	1.94/3.0.6.267
FRU/SDR	1.02
OS	Ubuntu 16.04 LTS
Test Tool	Stressapptest 1.0.7

Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Reset Cycle	1 DIMM Per Channel when applicable	Pass
4a. Memory Stress	DIMM Loading per spec 45°C 24 Hours	Pass
4c. Reset Cycle		200 loops per config

Note:

Memory Module Image



AVL USE ONLY:

Completed by:	Van Nguyen	Completion Date:	5/2/2018	AVL A#	U181105	AVL W/O	WF0964
Comments:							