



# Advanced Validation Labs, Inc.

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## Server Memory Compatibility Test Certificate

Test System: **Supermicro X11DPU (System SYS-6029U-TR4T)** Test Result: **Pass**

Leveraged Board(s) / System(s): X11DPU, X11DPU-X, X11DPU-XLL, X11DPU-Z+, SYS-1029U-TN10RT, SYS-1029U-TR4T, SYS-1029U-TRT, SYS-1029U-TR25M, SYS-1029U-TRTP2, SYS-1029U-TRTP, SYS-1029U-TR4, SYS-1029U-E1CR4T, SYS-1029U-E1CR4, SYS-1029U-E1CRT, SYS-1029U-E1CRTP, SYS-1029U-E1CRTP2, SYS-1029U-E1CR25M, SYS-6019U-TN4R4T, SYS-6019U-TN4RT, SYS-6019U-TR4T, SYS-6019U-TR4, SYS-6019U-TRTP, SYS-6019U-TRTP2, SYS-6019U-TR25M, SYS-6019U-TRT, SYS-2029U-TN24R4T, SYS-2029U-TR4T, SYS-2029U-TR4, SYS-2029U-TRTP, SYS-2029U-TRT, SYS-2029U-TR25M, SYS-2029U-E1CR4T, SYS-2029U-E1CR4, SYS-2029U-E1CRTP, SYS-2029U-E1CRT, SYS-2029U-E1CR25M, SYS-6029U-TR4T, SYS-6029U-TR4, SYS-6029U-TRT, SYS-6029U-TRTP, SYS-6029U-TR25M, SYS-6029U-E1CR4T, SYS-6029U-E1CR4, SYS-6029U-E1CRT, SYS-6029U-E1CRTP, SYS-6029U-E1CR25M, SYS-1029UZ-TN20R25M

### Module Specifications

Rev. 3/27/2018

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM24RD8/16MEI	RDIMM	1.2V	16GB	2Gx72	2666	19	E2	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code	Register Vendor / Rev.	DIMM Composition					
Kingston	MT40A1G8SA-075:E	8Gb	1024Mx8bit	1804	IDT	C0	(2048Mx8)*72		

### System Configuration

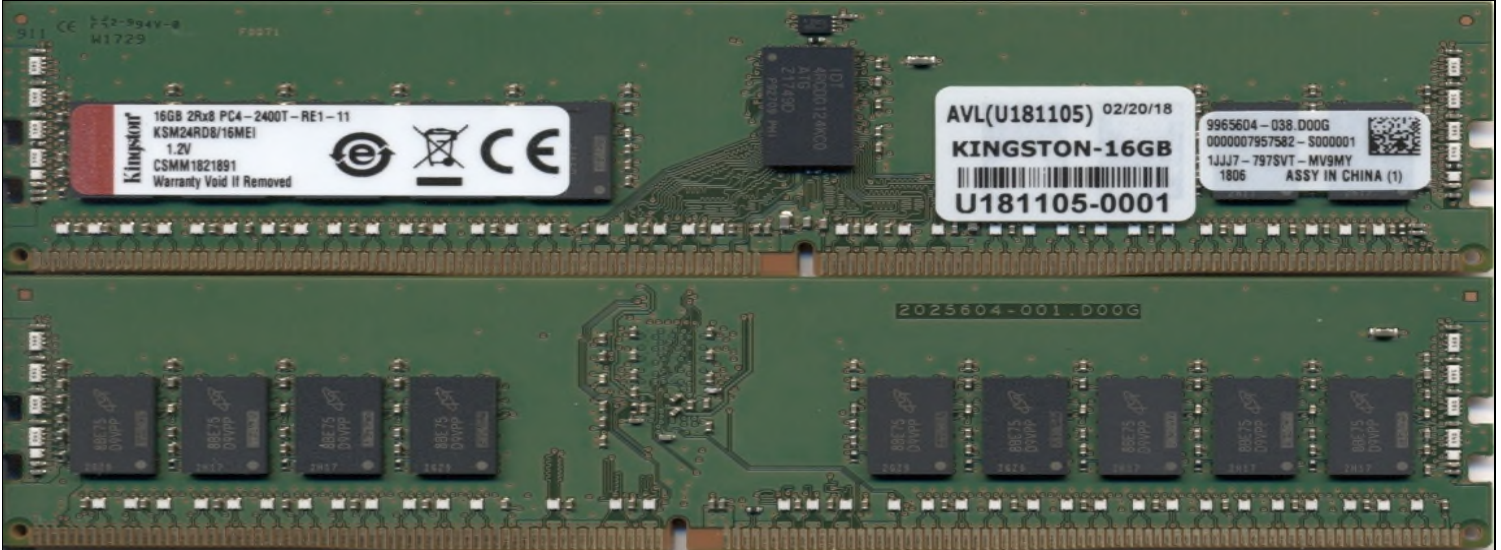
SETUP	System Information
AVL S/N	H180198-001
System S/N	CD0002
Board Rev. (PBA)	1.10
CPU Type	Intel Xeon Gold 6130(SKX) x2
Chipset	Skylake
BIOS / Date	2.0b /2/24/18
BMC / ME	1.43
FRU/SDR	n/a
OS	Ubuntu 16.04 LTS
Test Tool	Stressapptest 1.0.7

### Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Reset Cycle	1 DIMM Per Channel when applicable	Pass
4a. Memory Stress	DIMM Loading per spec 45°C	24 Hours Pass
4c. Reset Cycle		200 loops per config Pass

Note:

### Memory Module Image



### AVL USE ONLY:

Completed by:	Van Nguyen	Completion Date:	5/22/2018	AVL A#	U181105	AVL W/O	WF1110
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Comments: