

**Server Memory Compatibility Test Certificate**

Test System: **Supermicro X10DRi-LN4+**

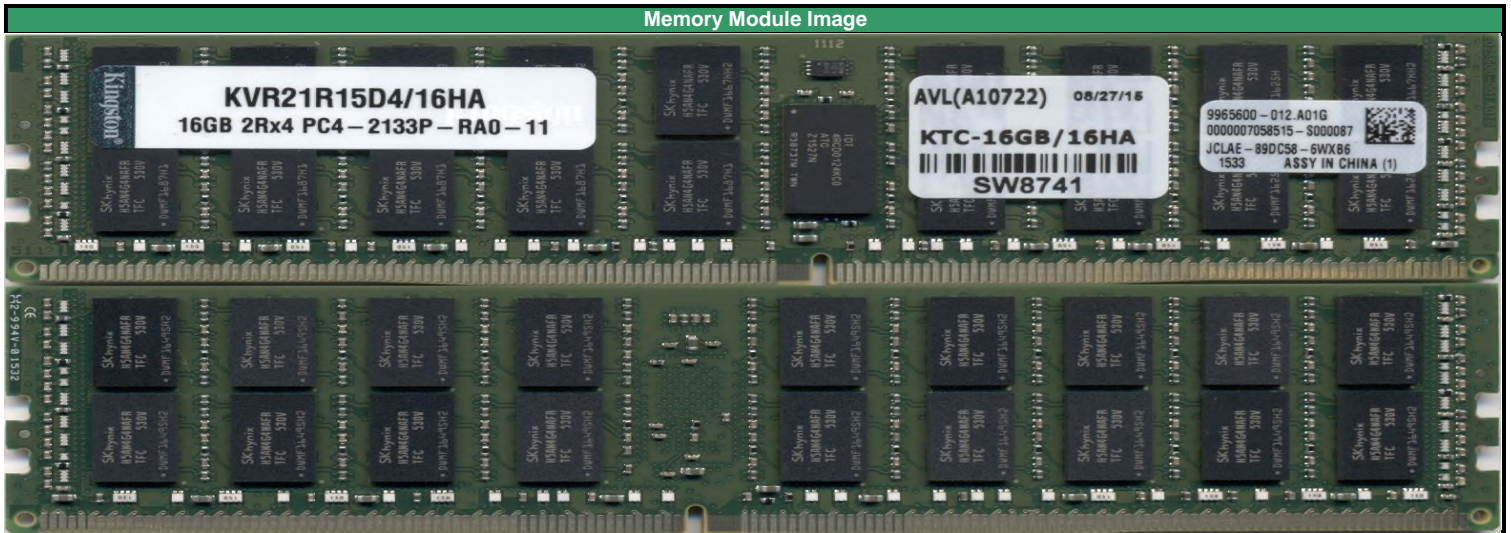
Test Result: **Pass**

Leveraged System(s): X10DRi-T4+, X10DRC-T4+ / X10DRC-LN4+, X10DRU-i+

Module Specifications										Rev. 1/13/2015
<b>DIMM Vendor</b>	<b>DIMM Part Number</b>	<b>Type</b>	<b>Voltage</b>	<b>Size</b>	<b>Config.</b>	<b>Speed</b>	<b>CL</b>	<b>R/C</b>	<b>Rank</b>	
Kingston	KVR21R15D4/16HA	RDIMM	1.20V	16GB	2Gx72	2133	9	A	DR	
<b>DRAM Vendor</b>	<b>DRAM Part Number</b>	<b>DRAM Density / Width / Date Code</b>			<b>Register Vendor / Rev.</b>		<b>DIMM Composition</b>			
Sk Hynix	KVR21R15D4/16HA	4Gb	1024Mx4bit	1530	IDT	C0	(1024Mx4)*72			

System Configuration	
<b>SETUP</b>	<b>System Information</b>
AVL S/N	SV2272
System S/N	VM148S014767
Board Rev. (PBA)	1.01
CPU Type	E5-2650 V3 x2, 2.3GHz
Chipset	Intel C612
BIOS / Date	1.0b / 1/29/15
BMC / ME	1.85/3.06.267
FRU/SDR	N/A
OS	Redhat 6.5
Test Tool	SAT, Memory Stress v1077

Testing Summary		
<b>Test Items</b>	<b>Test Description</b>	<b>Test Results</b>
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	<b>Done</b>
2. SPD Check	DIMM SPD content check for JEDEC compliance	<b>Pass</b>
3. Reset Cycle	1 DIMM Per Channel when applicable	<b>Pass</b>
4a. Memory Stress	DIMM Loading per spec	8 hours per config
4b. Memory Benchmark		5 loops per config
4c. Reset Cycle		200 loops per config
5. Functional Stress Test		12 hours
		<b>Pass</b>
Note:		



AVL USE ONLY:							
Completed by:	Van Nguyen	Completion Date:	9/10/2015	AVL A#	10722	AVL W/O	WD4077
Comments:							