

Server Memory Compatibility Test Certificate

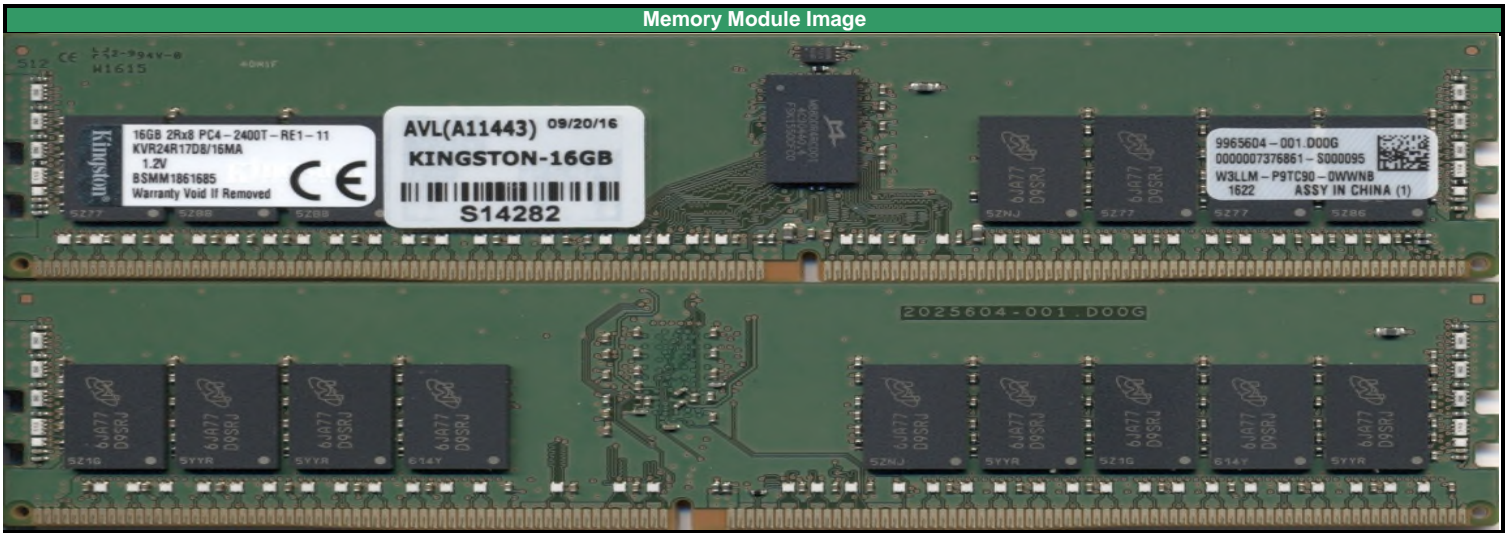
Test System: **Supermicro X10DRi** Test Result: **Pass**

Leveraged System(s): X10DRI-T, X10DAi, X10DAC, X10DRH-CT, X10DRW-i / X10DRW-iT, X10DDW-I / X10DDW-iN, X10DRG-H / X10DRG-HT, X10DRT-P / X10DRT-PT, X10DRT-PIBQ / X10DRT-PIBF, X10DRFR / X10DRFR-T / X10DRH-C, X10DRH-CLN4, X10DRH-iLN4, X10DRT-PS, X10DRX, X10DRU-X, X10DRU-XLL, X10DRFF-CG, X10DRFF-CTG, X10DRFF-iG, X10DRFF-

Module Specifications										Rev. 1/13/2015
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank	
Kingston	KVR24R17D8/16MA	PC4-2400 RDIMM	1.20V	16GB	2Gx72	2400	16	E	DR	
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code		Register Vendor / Rev.		DIMM Composition				
Micron	MT40A1G8PM-083E:A	8Gb 1024Mx8bit 1620		Montage C0		(1024x8)*72				

System Configuration	
SETUP	System Information
AVL S/N	SV2271
System S/N	0M148004057
Board Rev. (PBA)	1.01
CPU Type	Intel E5-2650 v4 (BDW) x2
Chipset	Intel C612
BIOS / Date	2.0 / 12/28/15
BMC / ME	1.94/3.0.6.267
FRU/SDR	1.02
OS	Linux Redhat 6.5
Test Tool	SAT, Memory Stress v1077

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Reset Cycle	1 DIMM Per Channel when applicable	Pass
4a. Memory Stress	DIMM Loading per spec	8 hours per config
4b. Memory Benchmark		5 loops per config
4c. Reset Cycle		200 loops per config
5. Functional Stress Test		12 hours
Note:		



AVL USE ONLY:							
Completed by:	Van Nguyen	Completion Date:	9/22/2016	AVL A#	11443	AVL W/O	WD6824
Comments:							