



Advanced Validation Labs, Inc.

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| Intel PCSD Server Memory Compatibility Test Certificate | |
|---------------------------------------------------------|--------------------------|
| Test System: Intel S1200SPR (Silver Pass) | Test Result: Pass |

Leveraged System(s): S1200SPLR; S1200SPOR; S1200SPSR

| Modules Information | | | | | | | | | |
|---------------------|------------------|----------------------------------|---------|------|------------------------|-------|------------------|-----|------|
| DIMM Vendor | DIMM Part Number | Type | Voltage | Size | Config. | Speed | CL | R/C | Rank |
| Kingston | KSM24ED8/16ME | UDIMM ECC | 1.2V | 16GB | 2Gx72 | 2400 | 17 | E | DR |
| DRAM Vendor | DRAM Part Number | DRAM Density / Width / Date Code | | | Register Vendor / Rev. | | DIMM Composition | | |
| Micron | MT40A1G8SA-075:E | 8Gb 1024Mx8bit 1804 | | | | | (1024Mx8)*2*72 | | |

| System Configuration | | |
|----------------------|-----------------------------------------|-----------------------|
| SETUP | System #1 | System #2 |
| AVL S/N | SY9443 | SY9444 |
| System S/N | QSSA54400007 / LVPP | QSSA54400010 / HVPP |
| Board Rev. (PBA) | H57532-208 | |
| CPU Type | E3-1225 v6 / 3.30 GHz | E3-1275 v6 / 3.80 GHz |
| Chipset | C232 | |
| BIOS | 03.01.1029 | |
| BMC / ME | 01.12.11072 / 04.01.04.054 | |
| FUR/SDR | 0.26 | |
| OS | Windows Server 2012 R2 | |
| Test Tool | iWVSS 2.7.0, SELViewer, Syscfg, WinPIRA | |

| Testing Summary | | |
|----------------------------------------------|--------------------------------------------------|--------------|
| Test Items | Test Description | Test Results |
| 1. Latest BIOS Upgrade & Configuration check | Record memory Size and Speed detection from BIOS | Done |
| 2. SPD Check | DIMM SPD content check for JEDEC compliance | Pass |
| 3. Memory Stress | Test for 6 hours @ Max and Min Loading | HVDD Hot |
| 4. Memory Stress | | HVDD Cold |
| 5. Memory Stress | | LVDD Hot |
| 6. Memory Stress | | LVDD Cold |
| Note: | | |

Memory Module Image



| AVL USE ONLY: | | | | | | | |
|---------------|------------|------------------|------------|--------|---------|---------|--------|
| Completed by: | Andy Chang | Completion Date: | 04/23/2018 | AVL A# | U181109 | AVL W/O | WF0878 |
| Comments: | | | | | | | |

Test Results

| 4C | | | | | |
|-----------------|------|----------------|---|---|---|
| Minimum Loading | | | | | |
| Start Date | | 4/16/2018 | | | |
| DIMM Voltage | | 1.22v / 1.16v | | | |
| DIMM VPP | | 2.64v / 2.422v | | | |
| DIMM | S/N | A | B | C | D |
| CPU1 A1 | 0005 | P | P | P | P |
| CPU1 A2 | | | | | |
| CPU1 B1 | 0006 | P | P | P | P |
| CPU1 B2 | | | | | |

| 4C | | | | | |
|-----------------|------|----------------|---|---|---|
| Maximum Loading | | | | | |
| Start Date | | 04/16/18 | | | |
| DIMM Voltage | | 1.22v / 1.16v | | | |
| DIMM VPP | | 2.64v / 2.422v | | | |
| DIMM | S/N | A | B | C | D |
| CPU1 A1 | 0001 | P | P | P | P |
| CPU1 A2 | 0002 | P | P | P | P |
| CPU1 B1 | 0003 | P | P | P | P |
| CPU1 B2 | 0004 | P | P | P | P |

