



Advanced Validation Labs, Inc.
17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



Intel PCSD Server Memory Compatibility Test Certificate

Test System: Intel S2600ST (SawTooth Pass)	Test Result: Pass
---	--------------------------

Leveraged System(s): BBS2600STQ,S2600ST,S2600STB,S2600STQ

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM26RD8/32MEI	RDIMM	1.2V	32GB	4Gx72	2666	19	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Micron	MT40A2G8JC-062E:E	16Gb	2048Mx8bit	2018	IDT	B1	(2048Mx8)*2*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	N/A	N/A
System S/N	FWSP95220798	FWSP01222140
Board Rev. (PBA)	J17012-552	
CPU Type	Intel CLX 2.50 GHz	Xeon Gold 6258R / 2.7 GHz
Chipset	Intel 62X Series	
BIOS	02.01.0013	
BMC / ME	2.48 / 04.01.04.423	
FUR/SDR	1.80	0.08
OS	Windows Server 2016 Standard	
Test Tool	iWVSS 2.9.2, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot Pass
4. Memory Stress		HVDD Cold Pass
5. Memory Stress		LVDD Hot Pass
6. Memory Stress		LVDD Cold Pass
Note:		

Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	10/01/2021	AVL A#	U201581	AVL W/O	WF8278
Comments:							

Test Results

4C Minimum Loading				
Start Date		9/17/2021		
DIMM Voltage		1.22v / 1.16v		
DIMM	S/N	A	B	C D
CPU1 A1	0001	P	P	P P
CPU1 A2				
CPU1 B1	0002	P	P	P P
CPU1 C1	0003	P	P	P P
CPU1 D1	0004	P	P	P P
CPU1 D2				
CPU1 E1	0005	P	P	P P
CPU1 F1	0006	P	P	P P
CPU2 G1	0007	P	P	P P
CPU2 G2				
CPU2 H1	0008	P	P	P P
CPU2 I1	0009	P	P	P P
CPU2 J1	0010	P	P	P P
CPU2 J2				
CPU2 K1	0011	P	P	P P
CPU2 L1	0012	P	P	P P

4C Maximum Loading				
Start Date		09/17/21		
DIMM Voltage		1.22v / 1.16v		
DIMM	S/N	A	B	C D
CPU1 A1	0001	P	P	P P
CPU1 A2	0002	P	P	P P
CPU1 B1	0003	P	P	P P
CPU1 C1	0004	P	P	P P
CPU1 D1	0005	P	P	P P
CPU1 D2	0006	P	P	P P
CPU1 E1	0007	P	P	P P
CPU1 F1	0008	P	P	P P
CPU2 G1	0009	P	P	P P
CPU2 G2	0010	P	P	P P
CPU2 H1	0011	P	P	P P
CPU2 I1	0012	P	P	P P
CPU2 J1	0013	P	P	P P
CPU2 J2	0014	P	P	P P
CPU2 K1	0015	P	P	P P
CPU2 L1	0016	P	P	P P