



Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600WF (Wolf Pass)	Test Result: Pass

Leveraged System(s): R1208WFTYS, R1304WF, R1304WF0YS, R1304WFTYS, R2000WF, R2208WF0ZS, R2208WFTZS, R2224WFTZS, R2308WFTZS, R2312WF0NP, R2312WFTZS, S2600WF0, S2600WFQ, S2600WFT

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM26RS4/16HCL	RDIMM	1.2V	16GB	2Gx72	2666	19	C	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.	DIMM Composition			
SK Hynix	H5AN8G4NCJR-WMC	8Gb	2048Mx4bit	1911	IDT	B1	(2048Mx4) * 72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	H170060-003	
System S/N	BQWF83600199	
Board Rev. (PBA)	H48104-863	
CPU Type	Intel CLX 2.20 GHz	
Chipset	Intel 62X Series	
BIOS	01.0261	01.0261
BMC / ME	1.83 / 04.01.03.237	1.83 / 04.01.03.237
FUR/SDR	1.74	1.74
OS	Windows Server 2016 Standard	
Test Tool	iWVSS 2.8.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot Pass
4. Memory Stress		HVDD Cold Pass
5. Memory Stress		LVDD Hot Pass
6. Memory Stress		LVDD Cold Pass
Note:		

Memory Module Image							
AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	08/23/2019	AVL A#	U191276	AVL W/O	WF2678
Comments:							

Test Results											
4C Minimum Loading				4C Maximum Loading							
Start Date	8/9/2019			Start Date	08/09/19						
DIMM Voltage	1.22v / 1.16v			DIMM Voltage	1.22v / 1.16v						
DIMM	S/N	A	B	C	D	DIMM	S/N	A	B	C	D
CPU1 A1	76-014	P	P	P	P	CPU1 A1	76-014	P	P	P	P
CPU1 A2						CPU1 A2	76-015	P	P	P	P
CPU1 B1	76-015	P	P	P	P	CPU1 B1	76-016	P	P	P	P
CPU1 B2						CPU1 B2	76-017	P	P	P	P
CPU1 C1	76-016	P	P	P	P	CPU1 C1	76-018	P	P	P	P
CPU1 C2						CPU1 C2	76-019	P	P	P	P
CPU1 D1	76-017	P	P	P	P	CPU1 D1	76-020	P	P	P	P
CPU1 D2						CPU1 D2	76-021	P	P	P	P
CPU1 E1	76-018	P	P	P	P	CPU1 E1	76-022	P	P	P	P
CPU1 E2						CPU1 E2	76-023	P	P	P	P
CPU1 F1	76-019	P	P	P	P	CPU1 F1	76-024	P	P	P	P
CPU1 F2						CPU1 F2	76-025	P	P	P	P
CPU2 G1	77-014	P	P	P	P	CPU2 G1	77-014	P	P	P	P
CPU2 G2						CPU2 G2	77-015	P	P	P	P
CPU2 H1	77-015	P	P	P	P	CPU2 H1	77-016	P	P	P	P
CPU2 H2						CPU2 H2	77-017	P	P	P	P
CPU2 I1	77-016	P	P	P	P	CPU2 I1	77-018	P	P	P	P
CPU2 I2						CPU2 I2	77-019	P	P	P	P
CPU2 J1	77-017	P	P	P	P	CPU2 J1	77-020	P	P	P	P
CPU2 J2						CPU2 J2	77-021	P	P	P	P
CPU2 K1	77-018	P	P	P	P	CPU2 K1	77-022	P	P	P	P
CPU2 K2						CPU2 K2	77-023	P	P	P	P
CPU2 L1	77-019	P	P	P	P	CPU2 L1	77-024	P	P	P	P
CPU2 L2						CPU2 L2	77-025	P	P	P	P