



**Advanced Validation Labs, Inc.**

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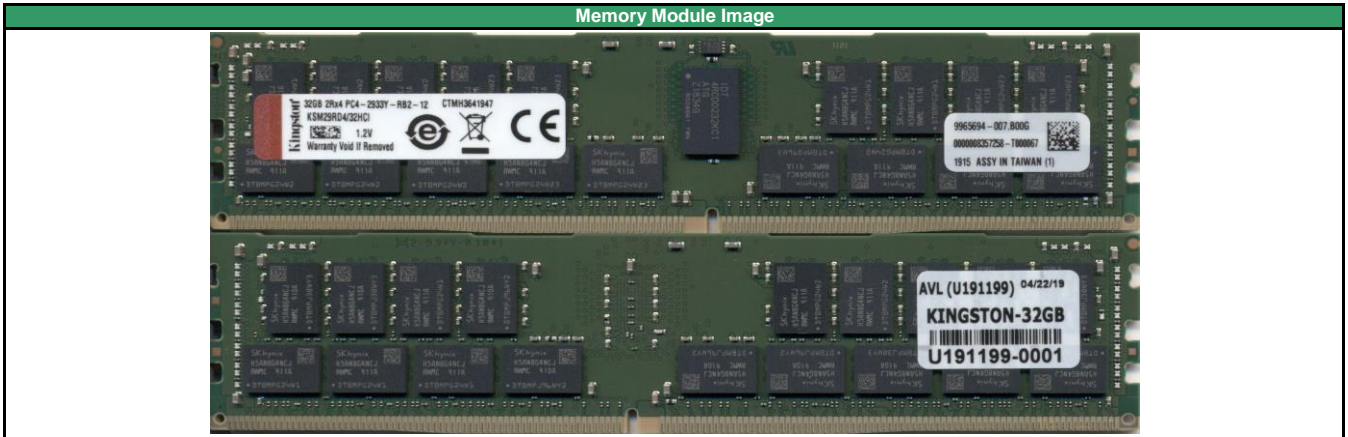
Intel PCSD Server Memory Compatibility Test Certificate	
Test System: <b>Intel S2600WF (Wolf Pass)</b>	Test Result: <b>Pass</b>

Leveraged System(s): R1208WFTYS, R1304WF, R1304WFOYS, R1304WFTYS, R2000WF, R2208WFOZS, R2208WFTZS, R2224WFTZS, R2308WFTZS, R2312WFOFP, R2312WFTZS, S2600WFO, S2600WFQ, S2600WFT

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM29RD4/32HCL	RDIMM	1.2V	32GB	4Gx72	2933	21	B2	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
SK Hynix	H5AN8G4NCJR-WMC	8Gb	2048Mx4bit	1911	IDT	C1	(2048Mx4)*2*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	H170060-003	
System S/N	BQWF83600199	
Board Rev. (PBA)	H48104-863	
CPU Type	Intel CLX 2.20 GHz	
Chipset	Intel 62X Series	
BIOS	01.0261	01.0261
BMC / ME	1.83 / 04.01.03.237	1.83 / 04.01.03.237
FUR/SDR	1.74	1.74
OS	Windows Server 2016 Standard	
Test Tool	iWVSS 2.8.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot <b>Pass</b>
4. Memory Stress		HVDD Cold <b>Pass</b>
5. Memory Stress		LVDD Hot <b>Pass</b>
6. Memory Stress		LVDD Cold <b>Pass</b>
Note:		



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	06/25/2019	AVL A#	U191199	AVL W/O	WF2543
Comments:							

Test Results											
4C Minimum Loading					4C Maximum Loading						
Start Date	6/11/2019				Start Date	06/11/19					
DIMM Voltage	1.22v / 1.16v				DIMM Voltage	1.22v / 1.16v					
DIMM	S/N	A	B	C	D	DIMM	S/N	A	B	C	D
CPU1 A1	99-014	P	P	P	P	CPU1 A1	99-014	P	P	P	P
CPU1 A2						CPU1 A2	99-015	P	P	P	P
CPU1 B1	99-015	P	P	P	P	CPU1 B1	99-016	P	P	P	P
CPU1 B2						CPU1 B2	99-017	P	P	P	P
CPU1 C1	99-016	P	P	P	P	CPU1 C1	99-018	P	P	P	P
CPU1 C2						CPU1 C2	99-019	P	P	P	P
CPU1 D1	99-017	P	P	P	P	CPU1 D1	99-020	P	P	P	P
CPU1 D2						CPU1 D2	99-021	P	P	P	P
CPU1 E1	99-018	P	P	P	P	CPU1 E1	99-022	P	P	P	P
CPU1 E2						CPU1 E2	99-023	P	P	P	P
CPU1 F1	99-019	P	P	P	P	CPU1 F1	99-024	P	P	P	P
CPU1 F2						CPU1 F2	99-025	P	P	P	P
CPU2 G1	00-014	P	P	P	P	CPU2 G1	00-014	P	P	P	P
CPU2 G2						CPU2 G2	00-015	P	P	P	P
CPU2 H1	00-015	P	P	P	P	CPU2 H1	00-016	P	P	P	P
CPU2 H2						CPU2 H2	00-017	P	P	P	P
CPU2 I1	00-016	P	P	P	P	CPU2 I1	00-018	P	P	P	P
CPU2 I2						CPU2 I2	00-019	P	P	P	P
CPU2 J1	00-017	P	P	P	P	CPU2 J1	00-020	P	P	P	P
CPU2 J2						CPU2 J2	00-021	P	P	P	P
CPU2 K1	00-018	P	P	P	P	CPU2 K1	00-022	P	P	P	P
CPU2 K2						CPU2 K2	00-023	P	P	P	P
CPU2 L1	00-019	P	P	P	P	CPU2 L1	00-024	P	P	P	P
CPU2 L2						CPU2 L2	00-025	P	P	P	P