



Advanced Validation Labs, Inc.

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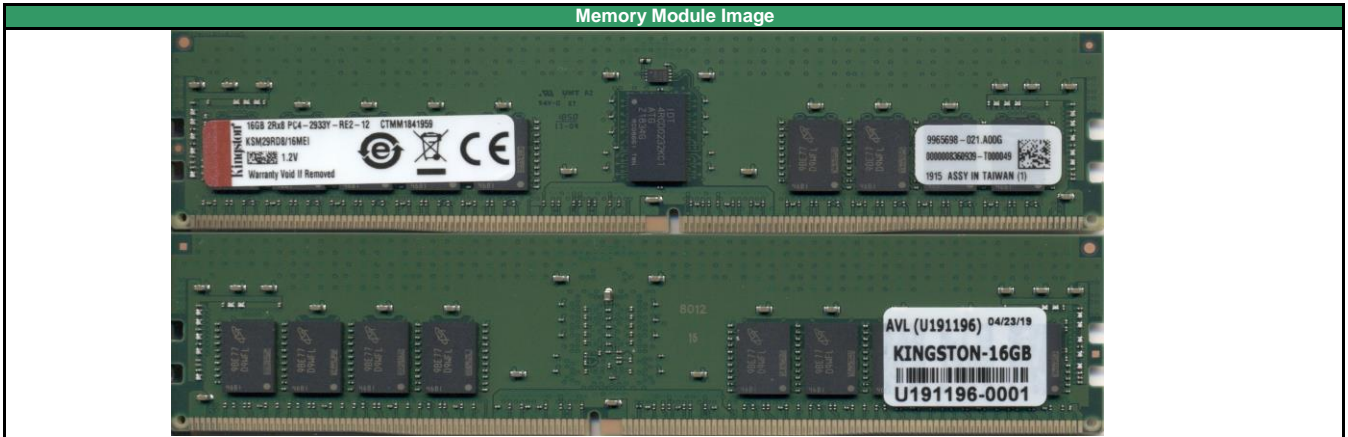
Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600WF (Wolf Pass)	Test Result: Pass

Leveraged System(s): R1208WFTYS, R1304WF, R1304WFOYS, R1304WFTYS, R2000WF, R2208WFOZS, R2208WFTZS, R2224WFTZS, R2308WFTZS, R2312WFOFP, R2312WFTZS, S2600WFO, S2600WFQ, S2600WFT

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM29RD8/16MEI	RDIMM	1.2V	16GB	2Gx72	2933	21	E2	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Micron	MT40A1G8SA-062E:E	8Gb	1024Mx8bit	1904	IDT	C1	(1024Mx8)*2*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N		
System S/N	BQWF83600199	
Board Rev. (PBA)	H48104-863	
CPU Type	Intel CLX 2.20 GHz	
Chipset	Intel 62X Series	
BIOS	01.0261	01.0261
BMC / ME	1.83 / 04.01.03.237	1.83 / 04.01.03.237
FUR/SDR	1.74	1.74
OS	Windows Server 2016 Standard	
Test Tool	iWVSS 2.8.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD Hot Pass
4. Memory Stress		HVDD Cold Pass
5. Memory Stress		LVDD Hot Pass
6. Memory Stress		LVDD Cold Pass
Note:		



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	06/17/2019	AVL A#	U191196	AVL W/O	WF2537
Comments:							

Test Results											
4C Minimum Loading					4C Maximum Loading						
Start Date	5/28/2019				Start Date	05/28/19					
DIMM Voltage	1.22v / 1.16v				DIMM Voltage	1.22v / 1.16v					
DIMM	S/N	A	B	C	D	DIMM	S/N	A	B	C	D
CPU1 A1	96-014	P	P	P	P	CPU1 A1	96-014	P	P	P	P
CPU1 A2						CPU1 A2	96-015	P	P	P	P
CPU1 B1	96-015	P	P	P	P	CPU1 B1	96-016	P	P	P	P
CPU1 B2						CPU1 B2	96-017	P	P	P	P
CPU1 C1	96-016	P	P	P	P	CPU1 C1	96-018	P	P	P	P
CPU1 C2						CPU1 C2	96-019	P	P	P	P
CPU1 D1	96-017	P	P	P	P	CPU1 D1	96-020	P	P	P	P
CPU1 D2						CPU1 D2	96-021	P	P	P	P
CPU1 E1	96-018	P	P	P	P	CPU1 E1	96-022	P	P	P	P
CPU1 E2						CPU1 E2	96-023	P	P	P	P
CPU1 F1	96-019	P	P	P	P	CPU1 F1	96-024	P	P	P	P
CPU1 F2						CPU1 F2	96-025	P	P	P	P
CPU2 G1	97-014	P	P	P	P	CPU2 G1	97-014	P	P	P	P
CPU2 G2						CPU2 G2	97-015	P	P	P	P
CPU2 H1	97-015	P	P	P	P	CPU2 H1	97-016	P	P	P	P
CPU2 H2						CPU2 H2	97-017	P	P	P	P
CPU2 I1	97-016	P	P	P	P	CPU2 I1	97-018	P	P	P	P
CPU2 I2						CPU2 I2	97-019	P	P	P	P
CPU2 J1	97-018	P	P	P	P	CPU2 J1	97-020	P	P	P	P
CPU2 J2						CPU2 J2	97-021	P	P	P	P
CPU2 K1	97-019	P	P	P	P	CPU2 K1	97-022	P	P	P	P
CPU2 K2						CPU2 K2	97-023	P	P	P	P
CPU2 L1	97-020	P	P	P	P	CPU2 L1	97-024	P	P	P	P
CPU2 L2						CPU2 L2	97-025	P	P	P	P