



Advanced Validation Labs, Inc.
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Intel PCSD Server Memory Compatibility Test Certificate

Test System: Intel S2600WT (Wildcat Pass)	Test Result: Pass
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Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WT2YSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2244WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTTR, S2600WTTTS1R

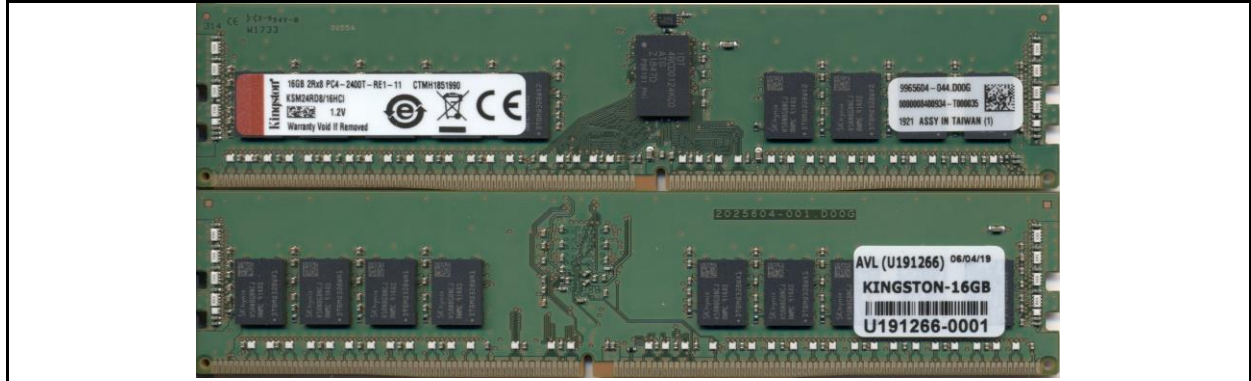
Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM24RD8/16HCI	RDIMM	1.2V	16GB	2Gx72	2400	17	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code	Register Vendor / Rev.	DIMM Composition					
SK Hynix	H5AN8G6NCJR-WMC	8Gb	1024Mx8bit	1914	IDT	C0	(1024Mx8)*272		

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS	01.01.0024	
BMC / ME	01.50.10802 / 03.01.03.043	
FUR/SDR	1.17	
OS	Windows Server 2012 R2	
Test Tool	iVWSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot Pass
4. Memory Stress		HVDD/HVPP Cold Pass
5. Memory Stress		LVDD/LVPP Hot Pass
6. Memory Stress		LVDD/LVPP Cold Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass

Note:

Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	01/20/2020	AVL A#	U191266	AVL W/O	WF2595

Comments:

Test Results

4C Minimum Loading						
Start Date		11/6/2019				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	66-017	P	P	P	P	
CPU1 A2						
CPU1 A3						
CPU1 B1	66-018	P	P	P	P	
CPU1 B2						
CPU1 B3						
CPU1 C1	66-019	P	P	P	P	
CPU1 C2						
CPU1 C3						
CPU1 D1	66-020	P	P	P	P	
CPU1 D2						
CPU1 D3						
CPU2 E1	67-017	P	P	P	P	
CPU2 E2						
CPU2 E3						
CPU2 F1	67-018	P	P	P	P	
CPU2 F2						
CPU2 F3						
CPU2 G1	67-019	P	P	P	P	
CPU2 G2						
CPU2 G3						
CPU2 H1	67-020	P	P	P	P	
CPU2 H2						
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	P

4C Middle Loading						
Start Date		11/06/19				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	66-013	P	P	P	P	
CPU1 A2	66-014	P	P	P	P	
CPU1 A3						
CPU1 B1	66-015	P	P	P	P	
CPU1 B2	66-016	P	P	P	P	
CPU1 B3						
CPU1 C1	66-017	P	P	P	P	
CPU1 C2	66-018	P	P	P	P	
CPU1 C3						
CPU1 D1	66-019	P	P	P	P	
CPU1 D2	66-020	P	P	P	P	
CPU1 D3						
CPU2 E1	67-013	P	P	P	P	
CPU2 E2	67-014	P	P	P	P	
CPU2 E3						
CPU2 F1	67-015	P	P	P	P	
CPU2 F2	67-016	P	P	P	P	
CPU2 F3						
CPU2 G1	67-017	P	P	P	P	
CPU2 G2	67-018	P	P	P	P	
CPU2 G3						
CPU2 H1	67-019	P	P	P	P	
CPU2 H2	67-020	P	P	P	P	
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	P

4C Maximum Loading						
Start Date		11/6/2019				
DIMM Voltage		1.22v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	66-001	P	P	P	P	
CPU1 A2	66-002	P	P	P	P	
CPU1 A3	66-003	P	P	P	P	
CPU1 B1	66-004	P	P	P	P	
CPU1 B2	66-005	P	P	P	P	
CPU1 B3	66-006	P	P	P	P	
CPU1 C1	66-007	P	P	P	P	
CPU1 C2	66-008	P	P	P	P	
CPU1 C3	66-009	P	P	P	P	
CPU1 D1	66-010	P	P	P	P	
CPU1 D2	66-011	P	P	P	P	
CPU1 D3	66-012	P	P	P	P	
CPU2 E1	67-001	P	P	P	P	
CPU2 E2	67-002	P	P	P	P	
CPU2 E3	67-003	P	P	P	P	
CPU2 F1	67-004	P	P	P	P	
CPU2 F2	67-005	P	P	P	P	
CPU2 F3	67-006	P	P	P	P	
CPU2 G1	67-007	P	P	P	P	
CPU2 G2	67-008	P	P	P	P	
CPU2 G3	67-009	P	P	P	P	
CPU2 H1	67-010	P	P	P	P	
CPU2 H2	67-011	P	P	P	P	
CPU2 H3	67-012	P	P	P	P	
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	P