



Advanced Validation Labs, Inc.

17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600WT (Wildcat Pass)	Test Result: Pass

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WT2YSR, R2208WTTYC1, R2208WTTYC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTTR, S2600WTTTS1R

Modules Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM24RS4/16MEI	RDIMM	1.2V	16GB	2Gx72	2400	17	C	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code		Register Vendor / Rev.		DIMM Composition			
Micron	MT40A2G4SA-075:E	8Gb	2048Mx4bit	1802	IDT	C0	(2048Mx4)*72		

System Configuration

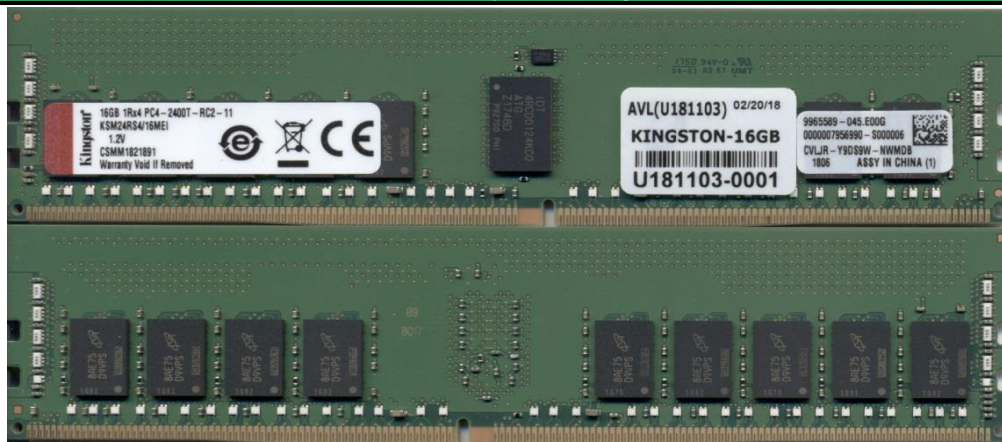
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS	01.01.0024	
BMC / ME	01.50.10802 / 03.01.03.043	
FUR/SDR	1.17	
OS	Windows Server 2012 R2	
Test Tool	iVVSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary

Test Items	Test Description	Test Results	
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done	
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass	
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot	Pass
4. Memory Stress		HVDD/HVPP Cold	Pass
5. Memory Stress		LVDD/LVPP Hot	Pass
6. Memory Stress		LVDD/LVPP Cold	Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass	

Note:

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	04/03/2018	AVL A#	U181103	AVL W/O	WF0899
---------------	------------	------------------	------------	--------	---------	---------	--------

Comments:

Test Results

4C					
Minimum Loading					
Start Date		3/19/2018			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	63-0001	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	63-0002	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	03-0001	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	03-0002	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	64-0001	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	64-0002	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	04-0001	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	04-0002	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		03/19/18			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	63-0001	P	P	P	P
CPU1 A2	63-0002	P	P	P	P
CPU1 A3					
CPU1 B1	63-0003	P	P	P	P
CPU1 B2	63-0004	P	P	P	P
CPU1 B3					
CPU1 C1	03-0001	P	P	P	P
CPU1 C2	03-0002	P	P	P	P
CPU1 C3					
CPU1 D1	03-0003	P	P	P	P
CPU1 D2	03-0004	P	P	P	P
CPU1 D3					
CPU2 E1	64-0001	P	P	P	P
CPU2 E2	64-0002	P	P	P	P
CPU2 E3					
CPU2 F1	64-0003	P	P	P	P
CPU2 F2	64-0004	P	P	P	P
CPU2 F3					
CPU2 G1	04-0001	P	P	P	P
CPU2 G2	04-0002	P	P	P	P
CPU2 G3					
CPU2 H1	04-0003	P	P	P	P
CPU2 H2	04-0004	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		3/19/2018			
DIMM Voltage		1.22v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	63-0001	P	P	P	P
CPU1 A2	63-0002	P	P	P	P
CPU1 A3	63-0003	P	P	P	P
CPU1 B1	63-0004	P	P	P	P
CPU1 B2	63-0005	P	P	P	P
CPU1 B3	63-0006	P	P	P	P
CPU1 C1	03-0001	P	P	P	P
CPU1 C2	03-0002	P	P	P	P
CPU1 C3	03-0003	P	P	P	P
CPU1 D1	03-0004	P	P	P	P
CPU1 D2	03-0005	P	P	P	P
CPU1 D3	03-0006	P	P	P	P
CPU2 E1	64-0001	P	P	P	P
CPU2 E2	64-0002	P	P	P	P
CPU2 E3	64-0003	P	P	P	P
CPU2 F1	64-0004	P	P	P	P
CPU2 F2	64-0005	P	P	P	P
CPU2 F3	64-0006	P	P	P	P
CPU2 G1	04-0001	P	P	P	P
CPU2 G2	04-0002	P	P	P	P
CPU2 G3	04-0003	P	P	P	P
CPU2 H1	04-0004	P	P	P	P
CPU2 H2	04-0005	P	P	P	P
CPU2 H3	04-0006	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P