



Advanced Validation Labs, Inc.
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Intel PCSD Server Memory Compatibility Test Certificate

Test System: Intel S2600WT (Wildcat Pass)	Test Result: Pass
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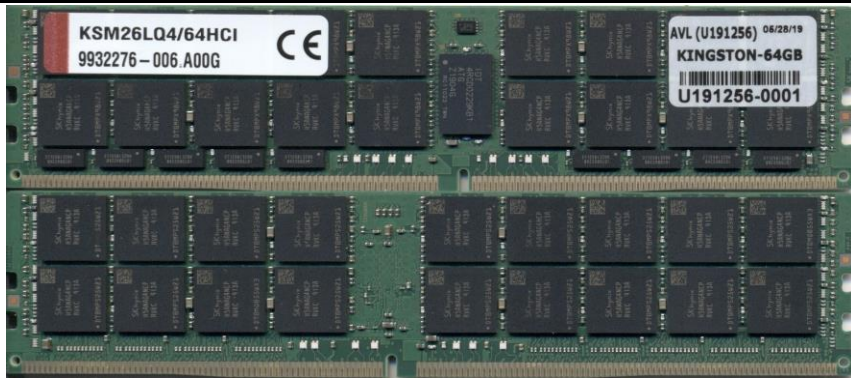
Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WT2YSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTT, S2600WTTT1R

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM26LQ4/64HCI	LRDIMM	1.2V	64GB	8Gx72	2666	19	D2	QR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code		Register Vendor / Rev.		DIMM Composition			
SK Hynix	H5ANAG4NCP-RVKC	8Gb		2048Mx4bit		1913		IDT B1 (2048Mx4)*472	

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS	01.01.0028	
BMC / ME	1.56 / 051	
FUR/SDR	1.18	
OS	Windows Server 2012 R2	
Test Tool	iVSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot Pass
4. Memory Stress		HVDD/HVPP Cold Pass
5. Memory Stress		LVDD/LVPP Hot Pass
6. Memory Stress		LVDD/LVPP Cold Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

Memory Module Image



AVL USE ONLY:						
Completed by:	Andy Chang	Completion Date:	08/14/2019	AVL A#	U191256	AVL W/O
						WF2494

Comments:

Test Results

4C Minimum Loading						
		7/19/2019				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	56-014	P	P	P	P	
CPU1 A2						
CPU1 A3						
CPU1 B1	56-015	P	P	P	P	
CPU1 B2						
CPU1 B3						
CPU1 C1	56-017	P	P	P	P	
CPU1 C2						
CPU1 C3						
CPU1 D1	56-018	P	P	P	P	
CPU1 D2						
CPU1 D3						
CPU2 E1	57-013	P	P	P	P	
CPU2 E2						
CPU2 E3						
CPU2 F1	57-014	P	P	P	P	
CPU2 F2						
CPU2 F3						
CPU2 G1	57-015	P	P	P	P	
CPU2 G2						
CPU2 G3						
CPU2 H1	57-016	P	P	P	P	
CPU2 H2						
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	P

4C Middle Loading						
		07/19/19				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	56-001	P	P	P	P	
CPU1 A2	56-002	P	P	P	P	
CPU1 A3						
CPU1 B1	56-003	P	P	P	P	
CPU1 B2	56-004	P	P	P	P	
CPU1 B3						
CPU1 C1	56-005	P	P	P	P	
CPU1 C2	56-006	P	P	P	P	
CPU1 C3						
CPU1 D1	56-007	P	P	P	P	
CPU1 D2	56-008	P	P	P	P	
CPU1 D3						
CPU2 E1	57-013	P	P	P	P	
CPU2 E2	57-014	P	P	P	P	
CPU2 E3						
CPU2 F1	57-015	P	P	P	P	
CPU2 F2	57-016	P	P	P	P	
CPU2 F3						
CPU2 G1	57-017	P	P	P	P	
CPU2 G2	57-018	P	P	P	P	
CPU2 G3						
CPU2 H1	57-019	P	P	P	P	
CPU2 H2	57-020	P	P	P	P	
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	P

4C Maximum Loading						
		7/19/2019				
DIMM Voltage		1.22v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	56-001	P	P	P	P	
CPU1 A2	56-002	P	P	P	P	
CPU1 A3	56-003	P	P	P	P	
CPU1 B1	56-004	P	P	P	P	
CPU1 B2	56-006	P	P	P	P	
CPU1 B3	56-007	P	P	P	P	
CPU1 C1	56-008	P	P	P	P	
CPU1 C2	56-009	P	P	P	P	
CPU1 C3	56-010	P	P	P	P	
CPU1 D1	56-011	P	P	P	P	
CPU1 D2	56-012	P	P	P	P	
CPU1 D3	56-013	P	P	P	P	
CPU2 E1	57-012	P	P	P	P	
CPU2 E2	57-013	P	P	P	P	
CPU2 E3	57-014	P	P	P	P	
CPU2 F1	57-015	P	P	P	P	
CPU2 F2	57-016	P	P	P	P	
CPU2 F3	57-017	P	P	P	P	
CPU2 G1	57-018	P	P	P	P	
CPU2 G2	57-019	P	P	P	P	
CPU2 G3	57-020	P	P	P	P	
CPU2 H1	57-021	P	P	P	P	
CPU2 H2	57-022	P	P	P	P	
CPU2 H3	57-023	P	P	P	P	
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	P