



Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600WT (Wildcat Pass)	Test Result: Pass

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WTTYSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTT, S2600WTTT1R

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM26RD4/32MEI	RDIMM	1.2V	32GB	4Gx72	2666	19	B	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code		Register Vendor / Rev.		DIMM Composition			
Micron	MT40A2G4SA-075:E	8Gb	2048Mx4bit	1802	IDT	B	(2048Mx4)*2*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS	01.01.0024	
BMC / ME	01.50.10802 / 03.01.03.043	
FUR/SDR	1.17	
OS	Windows Server 2012 R2	
Test Tool	iVVSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot
4. Memory Stress		HVDD/HVPP Cold
5. Memory Stress		LVDD/LVPP Hot
6. Memory Stress		LVDD/LVPP Cold
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

Memory Module Image



AVL USE ONLY:					
Completed by:	Andy Chang	Completion Date:	04/20/2018	AVL A#	U181061
				AVL W/O	WF1002

Comments:

Test Results

4C						
Minimum Loading						
Start Date		4/9/2018				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	61-0013	P	P	P	P	
CPU1 A2						
CPU1 A3						
CPU1 B1	61-0012	P	P	P	P	
CPU1 B2						
CPU1 B3						
CPU1 C1	61-0011	P	P	P	P	
CPU1 C2						
CPU1 C3						
CPU1 D1	61-0010	P	P	P	P	
CPU1 D2						
CPU1 D3						
CPU2 E1	62-0013	P	P	P	P	
CPU2 E2						
CPU2 E3						
CPU2 F1	62-0012	P	P	P	P	
CPU2 F2						
CPU2 F3						
CPU2 G1	62-0011	P	P	P	P	
CPU2 G2						
CPU2 G3						
CPU2 H1	62-0010	P	P	P	P	
CPU2 H2						
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	

4C						
Middle Loading						
Start Date		04/09/18				
DIMM Voltage		1.22v / 1.16v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	61-0013	P	P	P	P	
CPU1 A2	61-0012	P	P	P	P	
CPU1 A3						
CPU1 B1	61-0011	P	P	P	P	
CPU1 B2	61-0010	P	P	P	P	
CPU1 B3						
CPU1 C1	61-0009	P	P	P	P	
CPU1 C2	61-0008	P	P	P	P	
CPU1 C3						
CPU1 D1	61-0007	P	P	P	P	
CPU1 D2	61-0006	P	P	P	P	
CPU1 D3						
CPU2 E1	62-0013	P	P	P	P	
CPU2 E2	62-0012	P	P	P	P	
CPU2 E3						
CPU2 F1	62-0011	P	P	P	P	
CPU2 F2	62-0010	P	P	P	P	
CPU2 F3						
CPU2 G1	62-0009	P	P	P	P	
CPU2 G2	62-0008	P	P	P	P	
CPU2 G3						
CPU2 H1	62-0007	P	P	P	P	
CPU2 H2	62-0006	P	P	P	P	
CPU2 H3						
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	

4C						
Maximum Loading						
Start Date		4/9/2018				
DIMM Voltage		1.22v				
DIMM VPP		2.64v / 2.422v				
DIMM	S/N	A	B	C	D	
CPU1 A1	61-0001	P	P	P	P	
CPU1 A2	61-0002	P	P	P	P	
CPU1 A3	61-0003	P	P	P	P	
CPU1 B1	61-0004	P	P	P	P	
CPU1 B2	61-0005	P	P	P	P	
CPU1 B3	61-0006	P	P	P	P	
CPU1 C1	61-0007	P	P	P	P	
CPU1 C2	61-0008	P	P	P	P	
CPU1 C3	61-0009	P	P	P	P	
CPU1 D1	61-0010	P	P	P	P	
CPU1 D2	61-0011	P	P	P	P	
CPU1 D3	61-0012	P	P	P	P	
CPU2 E1	62-0001	P	P	P	P	
CPU2 E2	62-0002	P	P	P	P	
CPU2 E3	62-0003	P	P	P	P	
CPU2 F1	62-0004	P	P	P	P	
CPU2 F2	62-0005	P	P	P	P	
CPU2 F3	62-0006	P	P	P	P	
CPU2 G1	62-0007	P	P	P	P	
CPU2 G2	62-0008	P	P	P	P	
CPU2 G3	62-0009	P	P	P	P	
CPU2 H1	62-0010	P	P	P	P	
CPU2 H2	62-0011	P	P	P	P	
CPU2 H3	62-0012	P	P	P	P	
AC Power Cycling						
50 AC Cycles/corner		P	P	P	P	