



**Advanced Validation Labs, Inc.**  
17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



Intel PCSD Server Memory Compatibility Test Certificate	
Test System: <b>Intel S2600WT (Wildcat Pass)</b>	Test Result: <b>Pass</b>

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WT2YSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTTR, S2600WTTTS1R

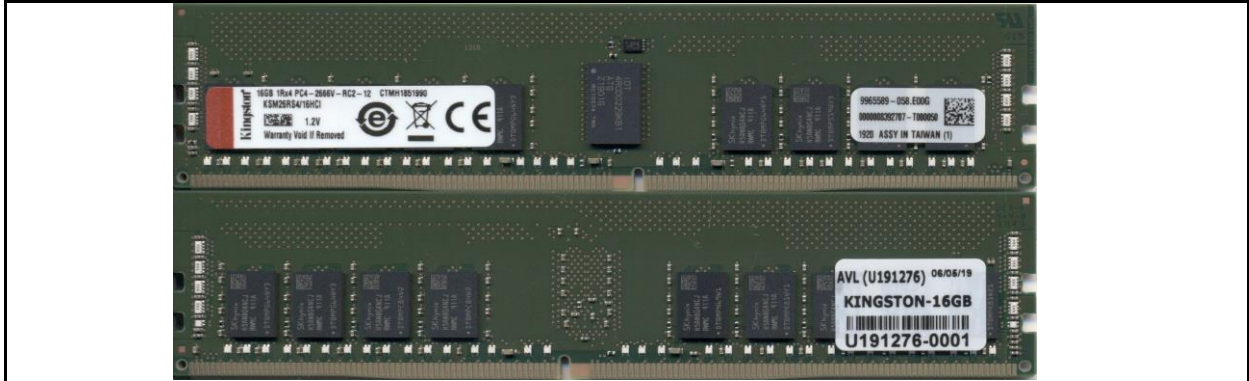
Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM26RS4/16HCL	RDIMM	1.2V	16GB	2Gx72	2666	19	C	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code	Register Vendor / Rev.	DIMM Composition					
SK Hynix	H5AN8G4NCJR-WMC	8Gb	2048Mx4bit	1911	IDT	B1	(2048Mx4)*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS	01.01.0028	
BMC / ME	1.56 / 051	
FUR/SDR	1.18	
OS	Windows Server 2012 R2	
Test Tool	iVSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot <b>Pass</b>
4. Memory Stress		HVDD/HVPP Cold <b>Pass</b>
5. Memory Stress		LVDD/LVPP Hot <b>Pass</b>
6. Memory Stress		LVDD/LVPP Cold <b>Pass</b>
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass

Note:

**Memory Module Image**



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	08/12/2019	AVL A#	U191276	AVL W/O	WF2684

Comments:

**Test Results**

4C Minimum Loading						
Start Date	7/15/2019					
DIMM Voltage	1.22v / 1.16v					
DIMM VPP	2.64v / 2.422v					
DIMM	S/N	A	B	C	D	
CPU1 A1	76-001	P	P	P	P	
CPU1 A2						
CPU1 A3						
CPU1 B1	76-002	P	P	P	P	
CPU1 B2						
CPU1 B3						
CPU1 C1	76-003	P	P	P	P	
CPU1 C2						
CPU1 C3						
CPU1 D1	76-004	P	P	P	P	
CPU1 D2						
CPU1 D3						
CPU2 E1	77-001	P	P	P	P	
CPU2 E2						
CPU2 E3						
CPU2 F1	77-002	P	P	P	P	
CPU2 F2						
CPU2 F3						
CPU2 G1	77-003	P	P	P	P	
CPU2 G2						
CPU2 G3						
CPU2 H1	77-004	P	P	P	P	
CPU2 H2						
CPU2 H3						
<b>AC Power Cycling</b>						
50 AC Cycles/corner						

4C Middle Loading						
Start Date	7/15/2019					
DIMM Voltage	1.22v / 1.16v					
DIMM VPP	2.64v / 2.422v					
DIMM	S/N	A	B	C	D	
CPU1 A1	76-005	P	P	P	P	
CPU1 A2	76-006	P	P	P	P	
CPU1 A3						
CPU1 B1	76-007	P	P	P	P	
CPU1 B2	76-008	P	P	P	P	
CPU1 B3						
CPU1 C1	76-009	P	P	P	P	
CPU1 C2	76-010	P	P	P	P	
CPU1 C3						
CPU1 D1	76-011	P	P	P	P	
CPU1 D2	76-012	P	P	P	P	
CPU1 D3						
CPU2 E1	77-018	P	P	P	P	
CPU2 E2	77-019	P	P	P	P	
CPU2 E3						
CPU2 F1	77-020	P	P	P	P	
CPU2 F2	77-021	P	P	P	P	
CPU2 F3						
CPU2 G1	77-022	P	P	P	P	
CPU2 G2	77-023	P	P	P	P	
CPU2 G3						
CPU2 H1	77-024	P	P	P	P	
CPU2 H2	77-025	P	P	P	P	
CPU2 H3						
<b>AC Power Cycling</b>						
50 AC Cycles/corner						

4C Maximum Loading						
Start Date	7/15/2019					
DIMM Voltage	1.22v					
DIMM VPP	2.64v / 2.422v					
DIMM	S/N	A	B	C	D	
CPU1 A1	76-001	P	P	P	P	
CPU1 A2	76-002	P	P	P	P	
CPU1 A3	76-003	P	P	P	P	
CPU1 B1	76-004	P	P	P	P	
CPU1 B2	76-005	P	P	P	P	
CPU1 B3	76-006	P	P	P	P	
CPU1 C1	76-007	P	P	P	P	
CPU1 C2	76-008	P	P	P	P	
CPU1 C3	76-009	P	P	P	P	
CPU1 D1	76-010	P	P	P	P	
CPU1 D2	76-011	P	P	P	P	
CPU1 D3	76-012	P	P	P	P	
CPU2 E1	77-001	P	P	P	P	
CPU2 E2	77-002	P	P	P	P	
CPU2 E3	77-003	P	P	P	P	
CPU2 F1	77-004	P	P	P	P	
CPU2 F2	77-005	P	P	P	P	
CPU2 F3	77-006	P	P	P	P	
CPU2 G1	77-007	P	P	P	P	
CPU2 G2	77-008	P	P	P	P	
CPU2 G3	77-009	P	P	P	P	
CPU2 H1	77-010	P	P	P	P	
CPU2 H2	77-011	P	P	P	P	
CPU2 H3	77-012	P	P	P	P	
<b>AC Power Cycling</b>						
50 AC Cycles/corner						