



Advanced Validation Labs, Inc.
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Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600WT (Wildcat Pass)** Test Result: **Pass**

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WT2YSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTTR, S2600WTTTS1R

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KSM26RS8/8HDI	RDIMM	1.2V	8GB	1Gx72	2666	19	D	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code	Register Vendor / Rev.	DIMM Composition					
SK Hynix	H5AN8G8NDJR-XNC	8Gb	1024Mx8bit	2013	IDT	B1	(1024Mx8)*72		

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS	01.01.0028	
BMC / ME	01.56 / 051	
FUR/SDR	1.18	
OS	Windows Server 2012 R2	
Test Tool	iVSS 2.7.0, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot Pass
4. Memory Stress		HVDD/HVPP Cold Pass
5. Memory Stress		LVDD/LVPP Hot Pass
6. Memory Stress		LVDD/LVPP Cold Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass

Memory Module Image

AVL USE ONLY:						
Completed by:	Andy Chang	Completion Date:	05/06/2021	AVL A#	U201583	AVL W/O
Comments:						

4C Minimum Loading												
Start Date		4/22/2021										
DIMM Voltage		1.22v / 1.16v										
DIMM VPP		2.64v / 2.422v										
DIMM	S/N	A	B	C	D							
CPU1 A1	0009	P	P	P	P							
CPU1 A2												
CPU1 A3												
CPU1 B1	0010	P	P	P	P							
CPU1 B2												
CPU1 B3												
CPU1 C1	0011	P	P	P	P							
CPU1 C2												
CPU1 C3												
CPU1 D1	0012	P	P	P	P							
CPU1 D2												
CPU1 D3												
CPU2 E1	0022	P	P	P	P							
CPU2 E2												
CPU2 E3												
CPU2 F1	0023	P	P	P	P							
CPU2 F2												
CPU2 F3												
CPU2 G1	0024	P	P	P	P							
CPU2 G2												
CPU2 G3												
CPU2 H1	0025	P	P	P	P							
CPU2 H2												
CPU2 H3												

4C Middle Loading												
Start Date		04/22/21										
DIMM Voltage		1.22v / 1.16v										
DIMM VPP		2.64v / 2.422v										
DIMM	S/N	A	B	C	D							
CPU1 A1	0001	P	P	P	P							
CPU1 A2	0002	P	P	P	P							
CPU1 A3												
CPU1 B1	0003	P	P	P	P							
CPU1 B2	0004	P	P	P	P							
CPU1 B3												
CPU1 C1	0005	P	P	P	P							
CPU1 C2	0006	P	P	P	P							
CPU1 C3												
CPU1 D1	0007	P	P	P	P							
CPU1 D2	0008	P	P	P	P							
CPU1 D3												
CPU2 E1	0009	P	P	P	P							
CPU2 E2	0010	P	P	P	P							
CPU2 E3												
CPU2 F1	0011	P	P	P	P							
CPU2 F2	0012	P	P	P	P							
CPU2 F3												
CPU2 G1	0013	P	P	P	P							
CPU2 G2	0014	P	P	P	P							
CPU2 G3												
CPU2 H1	0015	P	P	P	P							
CPU2 H2	0016	P	P	P	P							
CPU2 H3												

4C Maximum Loading												
Start Date		4/22/2021										
DIMM Voltage		1.22v										
DIMM VPP		2.64v / 2.422v										
DIMM	S/N	A	B	C	D							
CPU1 A1	0001	P	P	P	P							
CPU1 A2	0002	P	P	P	P							
CPU1 A3	0003	P	P	P	P							
CPU1 B1	0004	P	P	P	P							
CPU1 B2	0005	P	P	P	P							
CPU1 B3	0006	P	P	P	P							
CPU1 C1	0007	P	P	P	P							
CPU1 C2	0008	P	P	P	P							
CPU1 C3	0009	P	P	P	P							
CPU1 D1	0010	P	P	P	P							
CPU1 D2	0011	P	P	P	P							
CPU1 D3	0012	P	P	P	P							
CPU2 E1	0013	P	P	P	P							
CPU2 E2	0014	P	P	P	P							
CPU2 E3	0015	P	P	P	P							
CPU2 F1	0016	P	P	P	P							
CPU2 F2	0017	P	P	P	P							
CPU2 F3	0018	P	P	P	P							
CPU2 G1	0019	P	P	P	P							
CPU2 G2	0020	P	P	P	P							
CPU2 G3	0021	P	P	P	P							
CPU2 H1	0022	P	P	P	P							
CPU2 H2	0023	P	P	P	P							
CPU2 H3	0024	P	P	P	P							