



### Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate	
Test System: <b>Intel S2600WT (Wildcat Pass)</b>	Test Result: <b>Pass</b>

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WTTYSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTT, S2600WTTT1R

#### Modules Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR24R17D4/32L	RDIMM	1.2V	32GB	4Gx72	2400	17	B	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code		Register Vendor / Rev.		DIMM Composition			
Micron	MT40A2G4PM-083E:A	8Gb	2048Mx4bit	1606	Montage	C0	(2048Mx4)*2*72		

#### Leveraged Memory Modules

Vendor	Type	Voltage	CL	Speed
1 Kingston	KVR24R17D4K4/128L	RDIMM	1.2V	2400
2				
3				
4				
5				
6				

#### System Configuration

SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS / Date	01.01.0015 / 01-28-2016	
BMC / ME	01.43.9685 / 03.01.03.021	
FUR/SDR	1.12	
OS	Windows Server 2012 R2	
Test Tool	iWVSS 2.6.1, SELViewer, Syscfg, WinPIRA	

#### Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot <b>Pass</b>
4. Memory Stress		HVDD/HVPP Cold <b>Pass</b>
5. Memory Stress		LVDD/LVPP Hot <b>Pass</b>
6. Memory Stress		LVDD/LVPP Cold <b>Pass</b>
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

#### Memory Module Image



#### AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	06/24/16	AVL A#	A11309	AVL W/O	WD5949
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		6/7/2016			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ1060	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SZ1061	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SZ1062	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SZ1063	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SZ1064	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SZ1065	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SZ1044	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SZ1045	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		06/07/16			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ1044	P	P	P	P
CPU1 A2	SZ1045	P	P	P	P
CPU1 A3					
CPU1 B1	SZ1046	P	P	P	P
CPU1 B2	SZ1047	P	P	P	P
CPU1 B3					
CPU1 C1	SZ1048	P	P	P	P
CPU1 C2	SZ1049	P	P	P	P
CPU1 C3					
CPU1 D1	SZ1050	P	P	P	P
CPU1 D2	SZ1051	P	P	P	P
CPU1 D3					
CPU2 E1	SZ1052	P	P	P	P
CPU2 E2	SZ1053	P	P	P	P
CPU2 E3					
CPU2 F1	SZ1054	P	P	P	P
CPU2 F2	SZ1055	P	P	P	P
CPU2 F3					
CPU2 G1	SZ1056	P	P	P	P
CPU2 G2	SZ1057	P	P	P	P
CPU2 G3					
CPU2 H1	SZ1058	P	P	P	P
CPU2 H2	SZ1059	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		6/7/2016			
DIMM Voltage		1.22v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ1044	P	P	P	P
CPU1 A2	SZ1045	P	P	P	P
CPU1 A3	SZ1046	P	P	P	P
CPU1 B1	SZ1047	P	P	P	P
CPU1 B2	SZ1048	P	P	P	P
CPU1 B3	SZ1049	P	P	P	P
CPU1 C1	SZ1050	P	P	P	P
CPU1 C2	SZ1051	P	P	P	P
CPU1 C3	SZ1052	P	P	P	P
CPU1 D1	SZ1053	P	P	P	P
CPU1 D2	SZ1054	P	P	P	P
CPU1 D3	SZ1055	P	P	P	P
CPU2 E1	SZ1056	P	P	P	P
CPU2 E2	SZ1057	P	P	P	P
CPU2 E3	SZ1058	P	P	P	P
CPU2 F1	SZ1059	P	P	P	P
CPU2 F2	SZ1060	P	P	P	P
CPU2 F3	SZ1061	P	P	P	P
CPU2 G1	SZ1062	P	P	P	P
CPU2 G2	SZ1063	P	P	P	P
CPU2 G3	SZ1064	P	P	P	P
CPU2 H1	SZ1065	P	P	P	P
CPU2 H2	SZ1066	P	P	P	P
CPU2 H3	SZ1067	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P