



Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate	
Test System: Intel S2600WT (Wildcat Pass)	Test Result: Pass

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WTTYSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTT, S2600WTTTS1R

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR24R17D8/16I	RDIMM	1.2V	16GB	2Gx72	2400	17	E	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Micron	MT40A1G8PM-083E:A	8Gb	1024Mx8bit	1650	IDT	A	(1024Mx8)*2*72		

Leveraged Memory Modules						
Vendor			Type	Voltage	CL	Speed
1						
2						
3						
4						
5						
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS / Date	01.01.0015 / 01-28-2016	
BMC / ME	01.43.9685 / 03.01.03.021	
FUR/SDR	1.12	
OS	Windows Server 2012 R2	
Test Tool	iVSS 2.6.1, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot
4. Memory Stress		HVDD/HVPP Cold
5. Memory Stress		LVDD/LVPP Hot
6. Memory Stress		LVDD/LVPP Cold
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass

Note:

Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	04/27/2017	AVL A#	A11907	AVL W/O	WD7809
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		4/14/2017			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	S23360	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	S23361	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	S23362	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	S23363	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	S23364	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	S23365	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	S23366	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	S23367	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		4/14/2017			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	S23335	P	P	P	P
CPU1 A2	S23336	P	P	P	P
CPU1 A3					
CPU1 B1	S23337	P	P	P	P
CPU1 B2	S23338	P	P	P	P
CPU1 B3					
CPU1 C1	S23339	P	P	P	P
CPU1 C2	S23340	P	P	P	P
CPU1 C3					
CPU1 D1	S23341	P	P	P	P
CPU1 D2	S23342	P	P	P	P
CPU1 D3					
CPU2 E1	S23343	P	P	P	P
CPU2 E2	S23344	P	P	P	P
CPU2 E3					
CPU2 F1	S23345	P	P	P	P
CPU2 F2	S23346	P	P	P	P
CPU2 F3					
CPU2 G1	S23347	P	P	P	P
CPU2 G2	S23348	P	P	P	P
CPU2 G3					
CPU2 H1	S23349	P	P	P	P
CPU2 H2	S23350	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		4/14/2017			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	S23360	P	P	P	P
CPU1 A2	S23361	P	P	P	P
CPU1 A3	S23362	P	P	P	P
CPU1 B1	S23363	P	P	P	P
CPU1 B2	S23364	P	P	P	P
CPU1 B3	S23365	P	P	P	P
CPU1 C1	S23366	P	P	P	P
CPU1 C2	S23367	P	P	P	P
CPU1 C3	S23368	P	P	P	P
CPU1 D1	S23369	P	P	P	P
CPU1 D2	S23370	P	P	P	P
CPU1 D3	S23371	P	P	P	P
CPU2 E1	S23372	P	P	P	P
CPU2 E2	S23373	P	P	P	P
CPU2 E3	S23374	P	P	P	P
CPU2 F1	S23375	P	P	P	P
CPU2 F2	S23376	P	P	P	P
CPU2 F3	S23377	P	P	P	P
CPU2 G1	S23378	P	P	P	P
CPU2 G2	S23379	P	P	P	P
CPU2 G3	S23380	P	P	P	P
CPU2 H1	S23381	P	P	P	P
CPU2 H2	S23382	P	P	P	P
CPU2 H3	S23383	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P