



Advanced Validation Labs, Inc.

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Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600WT (Wildcat Pass)** Test Result: **Pass**

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WT2YSR, R2208WTTYC1, R2208WTTYC1R, R2208WTTYYS, R2208WTTYYSR, R2224WTTYYS, R2224WTTYYSR, R2308WTTGS, R2308WTTYYS, R2308WTTYYSR, R2312WTTYYS, R2312WTTYYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTTR, S2600WTTTS1R

Modules Information

DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR24R17S8/4I	RDIMM	1.2V	4GB	512Mx72	2400	17	D	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code		Register Vendor / Rev.		DIMM Composition			
Micron	MT40A512M8RH-083E:B	4Gb	512Mx8bit	1618	Inphi	B00G	(512Mx8)x2*72		

Leveraged Memory Modules

Vendor	Type	Voltage	CL	Speed
1 Kingston	KVR24R17S8K4/16I	RDIMM	1.2V	2400
2				
3				
4				
5				
6				

System Configuration

SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS / Date	01.01.0015 / 01-28-2016	
BMC / ME	01.43.9685 / 03.01.03.021	
FUR/SDR	1.12	
OS	Windows Server 2012 R2	
Test Tool	iWVSS 2.6.1, SELViewer, Syscfg, WinPIRA	

Testing Summary

Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot Pass
4. Memory Stress		HVDD/HVPP Cold Pass
5. Memory Stress		LVDD/LVPP Hot Pass
6. Memory Stress		LVDD/LVPP Cold Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

Memory Module Image



AVL USE ONLY:

Completed by:	Andy Chang	Completion Date:	08/19/16	AVL A#	A11354	AVL W/O	WD6061
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		7/25/2016			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ2727	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SZ2728	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SZ2729	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SZ2730	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SZ2731	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SZ2732	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SZ2733	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SZ2734	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		07/25/16			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ2711	P	P	P	P
CPU1 A2	SZ2712	P	P	P	P
CPU1 A3					
CPU1 B1	SZ2713	P	P	P	P
CPU1 B2	SZ2714	P	P	P	P
CPU1 B3					
CPU1 C1	SZ2715	P	P	P	P
CPU1 C2	SZ2716	P	P	P	P
CPU1 C3					
CPU1 D1	SZ2717	P	P	P	P
CPU1 D2	SZ2718	P	P	P	P
CPU1 D3					
CPU2 E1	SZ2719	P	P	P	P
CPU2 E2	SZ2720	P	P	P	P
CPU2 E3					
CPU2 F1	SZ2721	P	P	P	P
CPU2 F2	SZ2722	P	P	P	P
CPU2 F3					
CPU2 G1	SZ2723	P	P	P	P
CPU2 G2	SZ2724	P	P	P	P
CPU2 G3					
CPU2 H1	SZ2725	P	P	P	P
CPU2 H2	SZ2726	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		7/25/2016			
DIMM Voltage		1.22v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ2736	P	P	P	P
CPU1 A2	SZ2737	P	P	P	P
CPU1 A3	SZ2738	P	P	P	P
CPU1 B1	SZ2739	P	P	P	P
CPU1 B2	SZ2740	P	P	P	P
CPU1 B3	SZ2741	P	P	P	P
CPU1 C1	SZ2742	P	P	P	P
CPU1 C2	SZ2743	P	P	P	P
CPU1 C3	SZ2744	P	P	P	P
CPU1 D1	SZ2745	P	P	P	P
CPU1 D2	SZ2746	P	P	P	P
CPU1 D3	SZ2747	P	P	P	P
CPU2 E1	SZ2748	P	P	P	P
CPU2 E2	SZ2749	P	P	P	P
CPU2 E3	SZ2750	P	P	P	P
CPU2 F1	SZ2751	P	P	P	P
CPU2 F2	SZ2752	P	P	P	P
CPU2 F3	SZ2753	P	P	P	P
CPU2 G1	SZ2754	P	P	P	P
CPU2 G2	SZ2755	P	P	P	P
CPU2 G3	SZ2756	P	P	P	P
CPU2 H1	SZ2757	P	P	P	P
CPU2 H2	SZ2758	P	P	P	P
CPU2 H3	SZ2759	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P