



### Advanced Validation Labs, Inc.

17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



#### Intel PCSD Server Memory Compatibility Test Certificate

Test System: **Intel S2600WT (Wildcat Pass)** Test Result: **Pass**

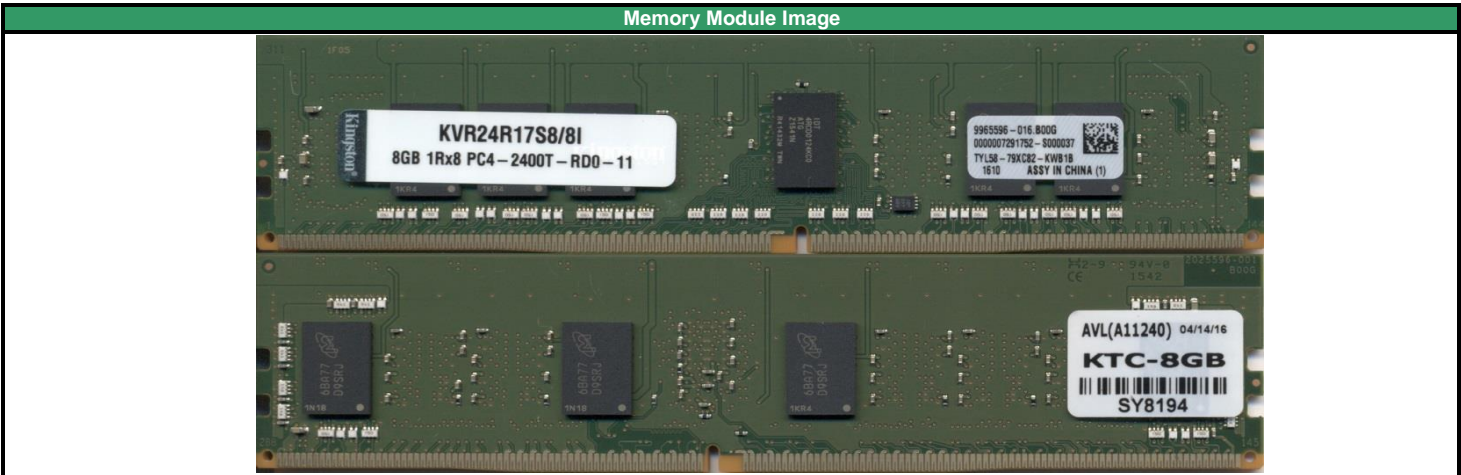
Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WTTYSR, R2208WTTTC1, R2208WTTTC1R, R2208WTTYS, R2208WTTYSR, R2224WTTYS, R2224WTTYSR, R2308WTTGS, R2308WTTYS, R2308WTTYSR, R2312WTTYS, R2312WTTYSR, R2312WTxxx, S2600WT, S2600WT2, S2600WT2R, S2600WTT, S2600WTTT, S2600WTTT1R

Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR24R17S8/8I	RDIMM	1.2V	8GB	1Gx72	2400	17	D	SR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code	Register Vendor / Rev.		DIMM Composition				
Micron	MT40A1G8PM-083E-A	8Gb	1024Mx8bit	1604	IDT	B	(1024Mx8)*72		

Leveraged Memory Modules						
Vendor	Part Number	Type	Voltage	CL	Speed	
1	Kingston	KVR24R17S8K4/32I	RDIMM	1.2V	17	2400
2						
3						
4						
5						
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS / Date	01.01.0015 / 01-28-2016	
BMC / ME	01.43.9685 / 03.01.03.021	
FUR/SDR	1.12	
OS	Windows Server 2012 R2	
Test Tool	iVSS 2.6.1, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot: Pass
4. Memory Stress		HVDD/HVPP Cold: Pass
5. Memory Stress		LVDD/LVPP Hot: Pass
6. Memory Stress		LVDD/LVPP Cold: Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	05/27/16	AVL A#	A11240	AVL W/O	WD5714
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		5/10/2016			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SY8219	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SY8220	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SY8221	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SY8222	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SY8223	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SY8224	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SY8225	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SY8226	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		05/10/16			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SY8194	P	P	P	P
CPU1 A2	SY8195	P	P	P	P
CPU1 A3					
CPU1 B1	SY8196	P	P	P	P
CPU1 B2	SY8197	P	P	P	P
CPU1 B3					
CPU1 C1	SY8198	P	P	P	P
CPU1 C2	SY8199	P	P	P	P
CPU1 C3					
CPU1 D1	SY8200	P	P	P	P
CPU1 D2	SY8201	P	P	P	P
CPU1 D3					
CPU2 E1	SY8202	P	P	P	P
CPU2 E2	SY8203	P	P	P	P
CPU2 E3					
CPU2 F1	SY8204	P	P	P	P
CPU2 F2	SY8205	P	P	P	P
CPU2 F3					
CPU2 G1	SY8206	P	P	P	P
CPU2 G2	SY8207	P	P	P	P
CPU2 G3					
CPU2 H1	SY8208	P	P	P	P
CPU2 H2	SY8209	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		5/10/2016			
DIMM Voltage		1.22v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SY8194	P	P	P	P
CPU1 A2	SY8195	P	P	P	P
CPU1 A3	SY8196	P	P	P	P
CPU1 B1	SY8197	P	P	P	P
CPU1 B2	SY8198	P	P	P	P
CPU1 B3	SY8199	P	P	P	P
CPU1 C1	SY8200	P	P	P	P
CPU1 C2	SY8201	P	P	P	P
CPU1 C3	SY8202	P	P	P	P
CPU1 D1	SY8203	P	P	P	P
CPU1 D2	SY8204	P	P	P	P
CPU1 D3	SY8205	P	P	P	P
CPU2 E1	SY8206	P	P	P	P
CPU2 E2	SY8207	P	P	P	P
CPU2 E3	SY8208	P	P	P	P
CPU2 F1	SY8209	P	P	P	P
CPU2 F2	SY8210	P	P	P	P
CPU2 F3	SY8211	P	P	P	P
CPU2 G1	SY8212	P	P	P	P
CPU2 G2	SY8213	P	P	P	P
CPU2 G3	SY8214	P	P	P	P
CPU2 H1	SY8215	P	P	P	P
CPU2 H2	SY8216	P	P	P	P
CPU2 H3	SY8217	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P