



### Advanced Validation Labs, Inc.

17665B Newhope Street, Fountain Valley, CA 92708 (714) 435-2630



Intel PCSD Server Memory Compatibility Test Certificate	
Test System: <b>Intel S2600WT (Wildcat Pass)</b>	Test Result: <b>Pass</b>

Leveraged System(s): MCB2208WAF4, MCB2208WAF5, MCB2312WHY2, R1208WT2GSR, R1208WTTGS, R1208WTxxx, R1208WTTGSR, R1280WTTGSSPP, R1304WT2GS, R1304WT2GSR, R1304WTTGS, R1304WTxxx, R2000WTxxx, R1304WTTGSR, R2208WT2YS, R2208WT2YSR, R2208WTTYC1, R2208WTTYC1R, R2208WTTYYS, R2208WTTYYSR,

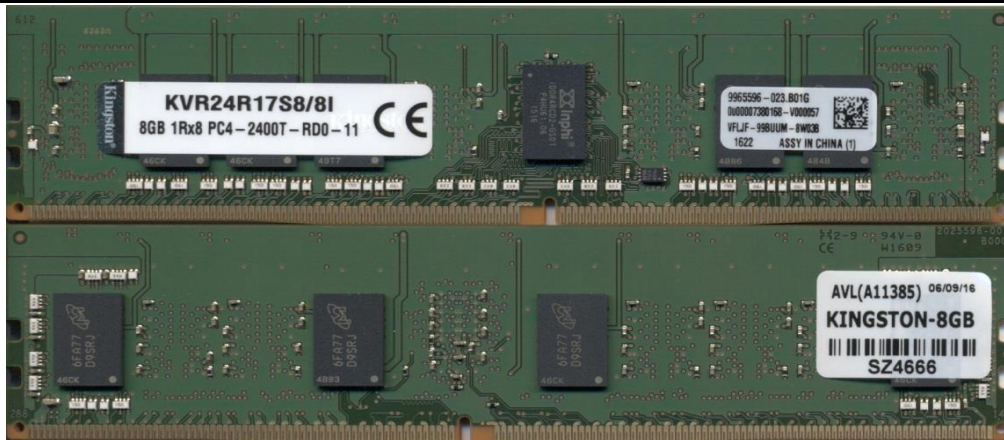
Modules Information									
DIMM Vendor	DIMM Part Number	Type	Voltage	Size	Config.	Speed	CL	R/C	Rank
Kingston	KVR24R17S8/8I	RDIMM	1.2V	8GB	1Gx72	2400	17	D	DR
DRAM Vendor	DRAM Part Number	DRAM Density / Width / Date Code			Register Vendor / Rev.		DIMM Composition		
Micron	MT40A1G8PM-083E:A	8Gb	1024Mx8bit	1612	Inphi	B00G	(1024Mx8)x2*72		

Leveraged Memory Modules						
Vendor	Type	Voltage	CL	Speed		
1 Kingston	KVR24R17S8K4/32I	RDIMM	1.2V	17	2400	
2						
3						
4						
5						
6						

System Configuration		
SETUP	System #1	System #2
AVL S/N	SV2346	SV2347
System S/N	BQWL42200131 / LVPP	BQWL42200377 / HVPP
Board Rev. (PBA)	G92187-300	
CPU Type	E5-2690 v4 / 2.60 GHz	
Chipset	C610	
BIOS / Date	01.01.0015 / 01-28-2016	
BMC / ME	01.43.9685 / 03.01.03.021	
FUR/SDR	1.12	
OS	Windows Server 2012 R2	
Test Tool	iWVSS 2.6.1, SELViewer, Syscfg, WinPIRA	

Testing Summary		
Test Items	Test Description	Test Results
1. Latest BIOS Upgrade & Configuration check	Record memory Size and Speed detection from BIOS	Done
2. SPD Check	DIMM SPD content check for JEDEC compliance	Pass
3. Memory Stress	Test for 6 hours @ Max and Min Loading	HVDD/HVPP Hot Pass
4. Memory Stress		HVDD/HVPP Cold Pass
5. Memory Stress		LVDD/LVPP Hot Pass
6. Memory Stress		LVDD/LVPP Cold Pass
6. Power Cycle	Test each corner for 50 cycle in room temp	Pass
Note:		

### Memory Module Image



AVL USE ONLY:							
Completed by:	Andy Chang	Completion Date:	08/12/16	AVL A#	A11385	AVL W/O	WD6195
Comments:							

Test Results

4C					
Minimum Loading					
Start Date		8/1/2016			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ4681	P	P	P	P
CPU1 A2					
CPU1 A3					
CPU1 B1	SZ4682	P	P	P	P
CPU1 B2					
CPU1 B3					
CPU1 C1	SZ4683	P	P	P	P
CPU1 C2					
CPU1 C3					
CPU1 D1	SZ4684	P	P	P	P
CPU1 D2					
CPU1 D3					
CPU2 E1	SZ4685	P	P	P	P
CPU2 E2					
CPU2 E3					
CPU2 F1	SZ4686	P	P	P	P
CPU2 F2					
CPU2 F3					
CPU2 G1	SZ4687	P	P	P	P
CPU2 G2					
CPU2 G3					
CPU2 H1	SZ4688	P	P	P	P
CPU2 H2					
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Middle Loading					
Start Date		08/01/16			
DIMM Voltage		1.22v / 1.16v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ4696	P	P	P	P
CPU1 A2	SZ4697	P	P	P	P
CPU1 A3					
CPU1 B1	SZ4698	P	P	P	P
CPU1 B2	SZ4699	P	P	P	P
CPU1 B3					
CPU1 C1	SZ4700	P	P	P	P
CPU1 C2	SZ4701	P	P	P	P
CPU1 C3					
CPU1 D1	SZ4702	P	P	P	P
CPU1 D2	SZ4703	P	P	P	P
CPU1 D3					
CPU2 E1	SZ4704	P	P	P	P
CPU2 E2	SZ4705	P	P	P	P
CPU2 E3					
CPU2 F1	SZ4706	P	P	P	P
CPU2 F2	SZ4707	P	P	P	P
CPU2 F3					
CPU2 G1	SZ4708	P	P	P	P
CPU2 G2	SZ4709	P	P	P	P
CPU2 G3					
CPU2 H1	SZ4710	P	P	P	P
CPU2 H2	SZ4711	P	P	P	P
CPU2 H3					
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P

4C					
Maximum Loading					
Start Date		8/1/2016			
DIMM Voltage		1.22v			
DIMM VPP		2.64v / 2.422v			
DIMM	S/N	A	B	C	D
CPU1 A1	SZ4691	P	P	P	P
CPU1 A2	SZ4692	P	P	P	P
CPU1 A3	SZ4693	P	P	P	P
CPU1 B1	SZ4694	P	P	P	P
CPU1 B2	SZ4695	P	P	P	P
CPU1 B3	SZ4696	P	P	P	P
CPU1 C1	SZ4697	P	P	P	P
CPU1 C2	SZ4698	P	P	P	P
CPU1 C3	SZ4699	P	P	P	P
CPU1 D1	SZ4700	P	P	P	P
CPU1 D2	SZ4701	P	P	P	P
CPU1 D3	SZ4702	P	P	P	P
CPU2 E1	SZ4703	P	P	P	P
CPU2 E2	SZ4704	P	P	P	P
CPU2 E3	SZ4705	P	P	P	P
CPU2 F1	SZ4706	P	P	P	P
CPU2 F2	SZ4707	P	P	P	P
CPU2 F3	SZ4708	P	P	P	P
CPU2 G1	SZ4709	P	P	P	P
CPU2 G2	SZ4710	P	P	P	P
CPU2 G3	SZ4711	P	P	P	P
CPU2 H1	SZ4712	P	P	P	P
CPU2 H2	SZ4713	P	P	P	P
CPU2 H3	SZ4714	P	P	P	P
AC Power Cycling					
50 AC Cycles/corner		P	P	P	P