

Product Change Notice



PCN: PCN_KVR21R15S4_8I_C2
Date: June 3, 2016

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USA

Type of Change: BOM Revisions

Description of Change: Multiple BOM revisions to reflect SPD bytes update. Certified BOMs for KVR21R15S4/8I and KVR21R15S4K4/32I have been revised to comply with a mandatory JEDEC specification update for SPD compliance. This change is a DDR4 SPD Revision 1.1 update, mandated by the JEDEC TG committee. Changes updated per the table below:

Application	Byte		Previous Revision		New Revision	
			Description	HEX	Description	HEX
2133MT/s	18	tCKAVGmin MTB	0.938ns	0x08	0.937ns	0x08
	125	tCKAVGmin FTB	0.938ns	0xC0	0.937ns	0xC1

The change is to correct the JEDEC SPD Revision 1.1 to have SPDs updated to the correct timing of 0.937ns for 2133MT/s operation. As the SPD has 1% rounding, the difference between 0.937ns and 0.938ns is negligible.

Product(s) Affected: KVR21R15S4/8I
KVR21R15S4K4/32I (Kit of 4)

BOM Numbers: 9965589-008.D01G > 9965589-008.D02G (Hynix A with IDT Register)
9965589-013.A00G > 9965589-013.A01G (Hynix A with Montage Register)

Product Description: 8GB DDR4-2133 Registered DIMM CL11 1Rx4 1.2V Intel Certified

New BOMs Available: June 3, 2016
Old BOMs Phase Out: August 2, 2016
