

## **e**MMC

# The perfect storage solution for mobile and embedded applications

Kingston® eMMC™ Flash memory follows the JEDEC eMMC 5.1 standard and encloses the NAND Flash and eMMC controller inside one JEDEC standardised package to provide a standard interface to the host CPU. The eMMC controller directs the Flash management, including ECC, wear-levelling, IOPS optimisation and read sensing, significantly reducing the storage management burden on the host CPU. A universal storage solution, Kingston eMMC is ideal for many electronic devices, including: smartphones, tablet PCs, eBook readers, electronic learning products, smart TVs, set-top boxes, smart home appliances and many wearable devices. Beyond its use in consumer products, the compact size, low power consumption and numerous enhanced features of eMMC mean that it is being rapidly adopted in many other embedded applications, such as Single Board Computers (SBC), robotics, medical devices, networking and building control devices. With the rapid growth of the IoT market, eMMC is finding its way to newer applications.

## **KEY BENEFITS**

- Simplifies system design and reduces time to market. The standard interface makes
  fast-changing NAND technology invisible to the host and the host processor
  doesn't have to keep changing its software to accommodate every NAND
  technology change and variation. This helps to significantly reduce the design-in
  complexity and shorten the qualification cycle.
- Helps to improve whole system performance. The eMMC controller frees up the host processor's valuable resources from NAND management so the host processor can use its processing power on other tasks.
- Provides a cost-effective solution. As opposed to SLC NAND, Kingston eMMC utilises MLC and 3D TLC NAND, making higher capacity storage for embedded applications much more affordable and enabling today's embedded designs to meet increasing demands for storage.
- Enhanced Mode (pSLC Mode) configuration available for better performance/endurance.

### **MARKET SEGMENTS**



Industrial IoT / robotics & factory automation



5G networking/telecommunications communication modules (Wi-Fi routers and mesh devices)



Wearables (smart watches, health monitors, AR & VR)



Smart home (sound bars, thermostats, fitness equipment, vacuums, beds, taps)



Smart city (HVAC, lighting, power monitoring/metering, parking meters)

### **eMMC PART NUMBERS AND SPECIFICATIONS**

| Part Number  | Capacity | <i>e</i> MMC<br>Standard | Package     | NAND   |
|--------------|----------|--------------------------|-------------|--------|
| EMMC04G-MT32 | 4GB      | 5.1 (HS400)              | 11.5x13x0.8 | MLC    |
| EMMC04G-CT32 | 4GB      | 5.1 (HS400)              | 9.0x7.5x0.8 | MLC    |
| EMMC08G-MV28 | 8GB      | 5.1 (HS400)              | 11.5x13x0.8 | MLC    |
| EMMC08G-CT32 | 8GB      | 5.1 (HS400)              | 9.0x7.5x0.8 | MLC    |
| EMMC16G-MW28 | 16GB     | 5.1 (HS400)              | 11.5x13x0.9 | 3D TLC |
| EMMC32G-TS0A | 32GB     | 5.1 (HS400)              | 11.5x13x1.0 | 3D TLC |
| EMMC64G-TB9F | 64GB     | 5.1 (HS400)              | 8.0x8.5x0.8 | 3D TLC |
| EMMC64G-TY29 | 64GB     | 5.1 (HS400)              | 11.5x13x0.8 | 3D TLC |
| EMMC128-TY29 | 128GB    | 5.1 (HS400)              | 11.5x13x0.8 | 3D TLC |
| EMMC256-TY29 | 256GB    | 5.1 (HS400)              | 11.5x13x1.0 | 3D TLC |

#### **KEY FEATURES**

| Boot Operation   Partitioning   Sleep Mode   Replay Protected Memory Block   Secure Trim/Secure Erase   Hardware Reset   Reliable Write   Background Operation   High Priority Interrupt   DDR Interface   Discard/Sanitise CMD   Packed Commands, Context IDs | √<br>√<br>√<br>√ |
|--|------------------|
| Sleep Mode  Replay Protected Memory Block  Secure Trim/Secure Erase  Hardware Reset  Reliable Write  Background Operation  High Priority Interrupt  DDR Interface  Discard/Sanitise CMD  Packed Commands, Context IDs  | ·<br>√           |
| Replay Protected Memory Block Secure Trim/Secure Erase  Hardware Reset  √ Reliable Write  Background Operation  High Priority Interrupt  DDR Interface  Discard/Sanitise CMD  Packed Commands, Context IDs   | ·<br>√           |
| Secure Trim/Secure Erase   | √<br>√           |
| Hardware Reset  √ Reliable Write  Background Operation  High Priority Interrupt  DDR Interface  Discard/Sanitise CMD  √ Packed Commands, Context IDs   | 1                |
| Reliable Write  Reliable Write  Background Operation  High Priority Interrupt  DDR Interface  Discard/Sanitise CMD  Packed Commands, Context IDs   | ٧                |
| Background Operation   | $\sqrt{}$        |
| High Priority Interrupt  DDR Interface  Discard/Sanitise CMD  Packed Commands, Context IDs  √  | $\checkmark$     |
| DDR Interface   Discard/Sanitise CMD   Packed Commands, Context IDs  | $\sqrt{}$        |
| Discard/Sanitise CMD   Packed Commands, Context IDs  √   | $\checkmark$     |
| Packed Commands, Context IDs  √  | $\checkmark$     |
| ·  | $\checkmark$     |
|  | $\checkmark$     |
| Power OFF Notification √   | $\sqrt{}$        |
| Data Tag √   | $\sqrt{}$        |
| Device Health Report $\sqrt{}$   | $\sqrt{}$        |
| Field FW Update √  | $\sqrt{}$        |
| Production State Awareness √   | $\checkmark$     |
| CMD Queuing  | 1                |
| Backward Compatibility √   |                  |

Learn more about the different eMMC versions:

eMMC 5.0: http://www.jedec.org/sites/default/files/docs/JESD84-B50.pdf eMMC 5.1: http://www.jedec.org/sites/default/files/docs/JESD84-B51.pdf For more information, including sample and quote requests, please visit kingston.com/emmc.



